

Evaluation of Subsystem Clock Oscillation Circuit

[HD64338124H-80A] QFP(14x14) 0.65mm pitch

Measurement conditions : 5.0V , 3.3V

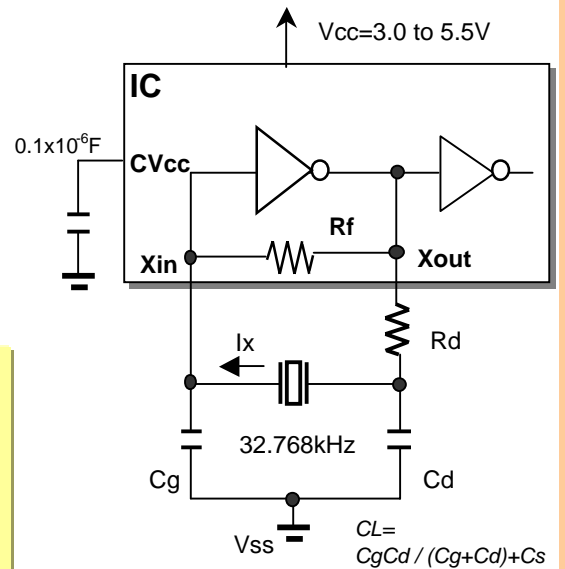
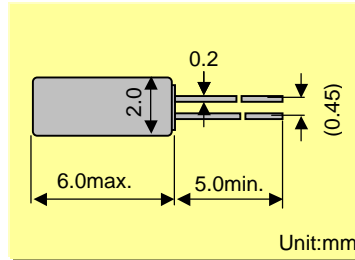


Model :VT-200
 Frequency :Fo=32.768kHz
 Frequency tolerance :dF/Fo= +/-20x10⁶
 Load capacitance :CL=6.0pF
 Equivalent series resistance :R1=50kohm max
 Max. Drive level :DL=1x10⁶W max
 Recommended drive level :DL=0.1x10⁶W typ

FEATURES

- 1.Compact tubular package
- 2.Photolithographic process
- 3.Excellent shock resistance and environmental characteristics.
- 4.Real time clocks, Timers, Portable applications

DIMENSIONS(VT-200)



Remark) Ix : current through crystal

When the internal power supply Step-Down circuit is not used, please connect the external power supply to the CVcc pin and Vcc pin.

The permissible range for the power supply voltage is 3.0V to 3.6V.

MODEL:VT-200 6.0pF with HD64338124H at 25°C

Key specifications	Vcc=3.3V	Vc=5.0V	Remarks
Current control resistance : Rd (k ohm)	0	0	Control drive level & secure phase margin
Capacitance at gate : Cg (pF)	7	7	Optimal capacity in response to CL
Capacitance at drain : Cd (pF)	7	7	(CL = Cd // Cg + stray capacitance)

Circuit characteristics (at 25°C)	Vcc=3.3V	Vc=5.0V	Remarks
Matching Accuracy : df / f (x10 ⁻⁶)	-5.2	-2.8	Frequency offset volume at specified Vdd
Voltage Fluctuation : +/-df / V (x10 ⁻⁶)	0.4	0.3	Vdd +/-10% (Standard operating voltage range)
Drive Level : DL (x10 ⁻⁶ W)	0.03	0.03	DL=Ix ² Re < 1x10 ⁻⁶ W, Re=R1(1 + Co / CL) ²
Negative resistance : - RL (kohm)	1137	1137	5 times larger than R _{1MAX}
Oscillation allowance : M (times)	22.7	22.7	Judgemental standard of oscillation stability
Oscillation start up time : Ts (sec)	0.46	0.43	Time to reach 90% of output level

Temperature characteristics of circuit	Vcc=3.3V	Vc=5.0V	Remarks
at -40°C Variation : df / T (x10 ⁻⁶)	-135	-135	Typ.Tp=25°C (K = -3.5x10 ⁻⁸ / °C ²)
at +85°C Variation : df / T (x10 ⁻⁶)	-138	-138	Typ.Tp=25°C (K = -3.5x10 ⁻⁸ / °C ²)

The mention value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics. Please review and check above parameters at customer's end.

Seiko Instruments USA Inc.

2990,West Lomita Blvd., Torrance, CA 90505, U.S.A
 Telephone :+1 310-517-7771 Facsimile :+1 310-517-7792
 Email :crystals@siu-la.com

Seiko Instruments GmbH

Siemensstrasse 9,D-63263 Neu-Isenburg,Germany
 Telephone :+49-6102-297-0 Facsimile :+49-6102-297-320
 Email :info@seiko-instruments.de

Seiko Instruments Inc.

1-8,Nakase,Mihama-ku,Chiba-shi,Chiba 261-8507,Japan
 Facsimile :+81-43-211-8030 E-mail :component@sii.co.jp



We value the "takumi" spirit.

Seiko Instruments Inc.
 Phone:+81-43-211-1207(Direct)

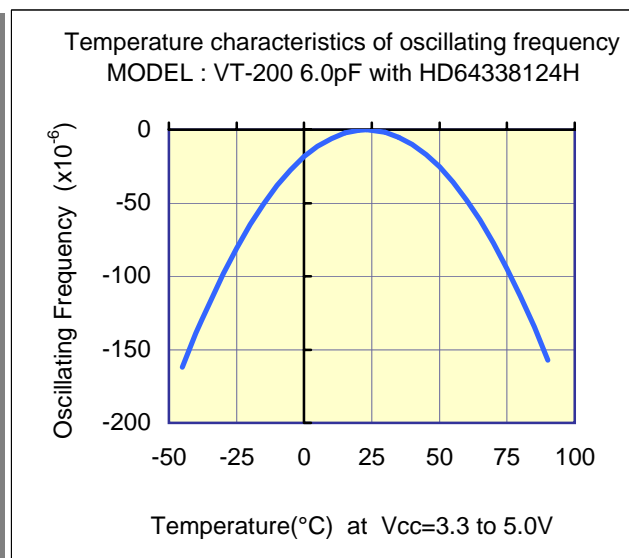
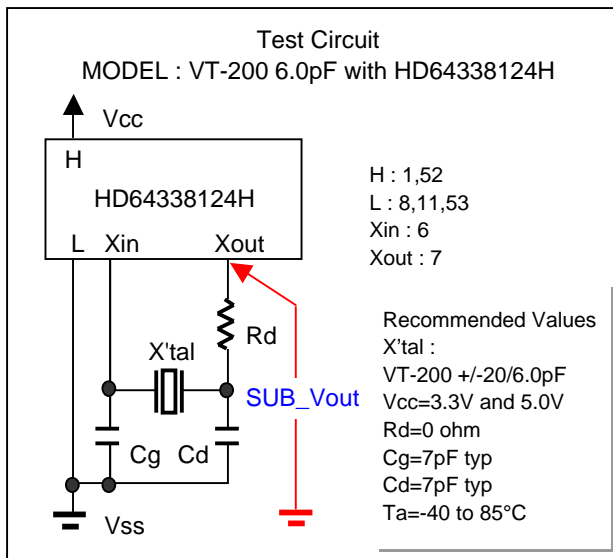
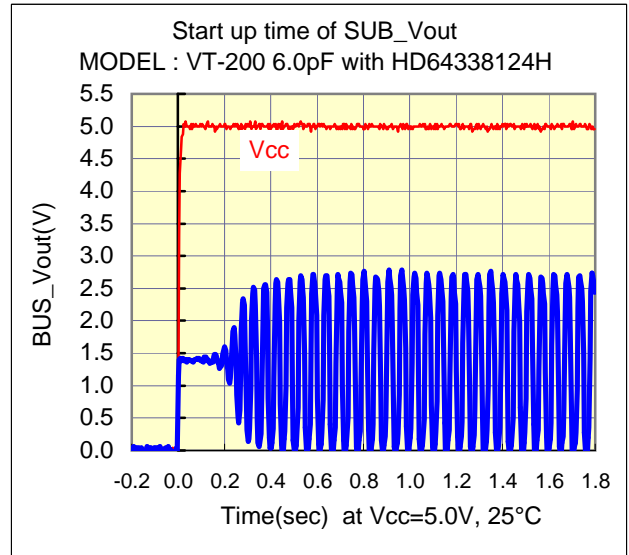
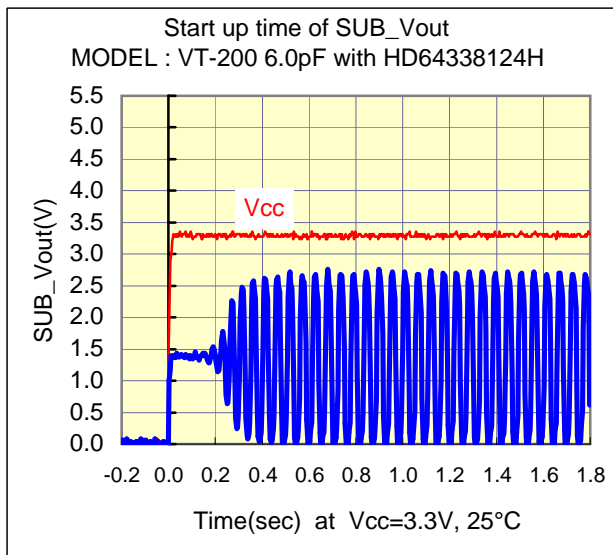
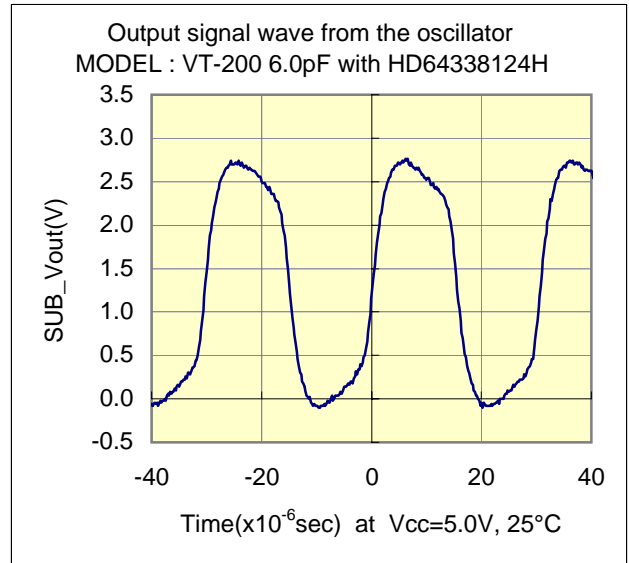
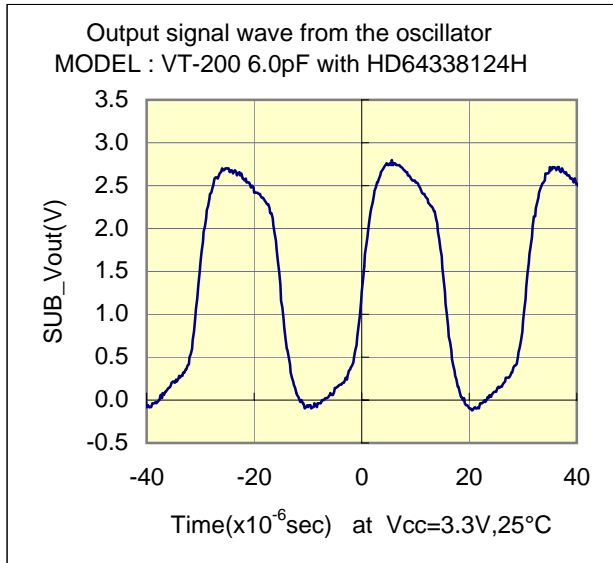
Evaluation of Subsystem Clock Oscillation Circuit

[HD64338124H-80A] QFP(14x14) 0.65mm pitch

Measurement conditions : 5.0V , 3.3V



Test Data



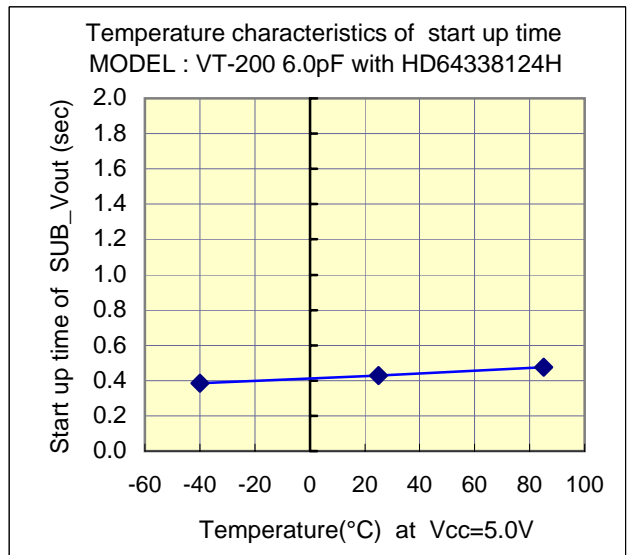
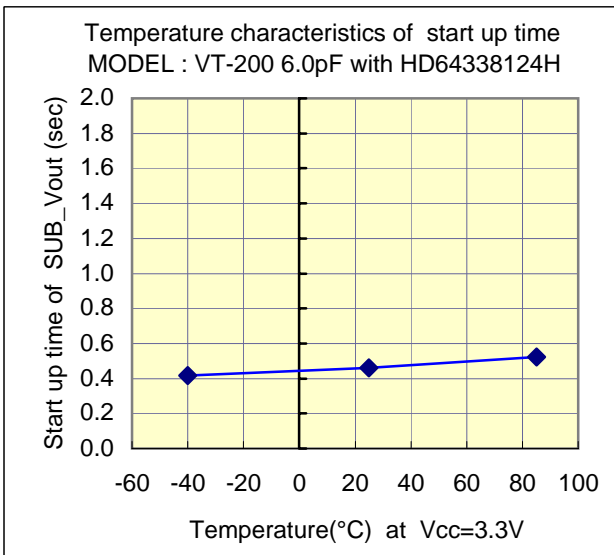
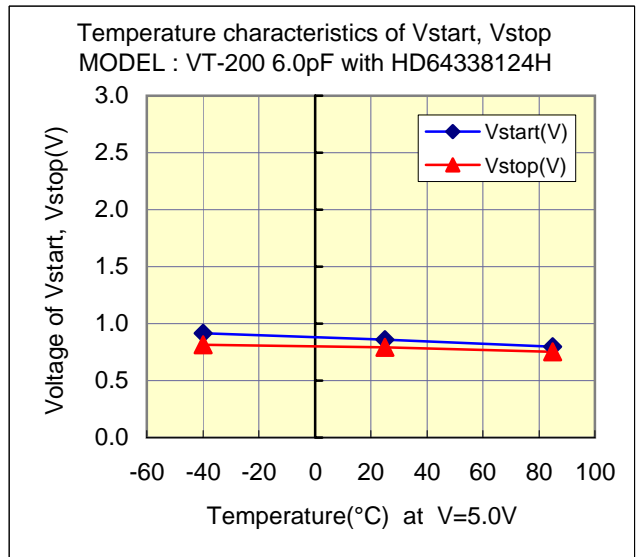
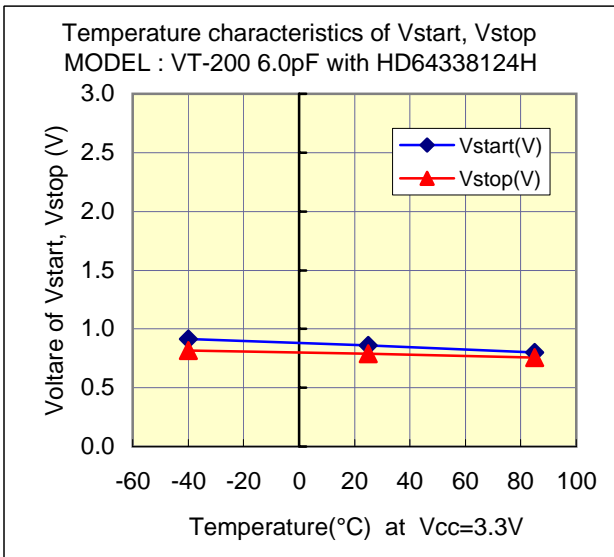
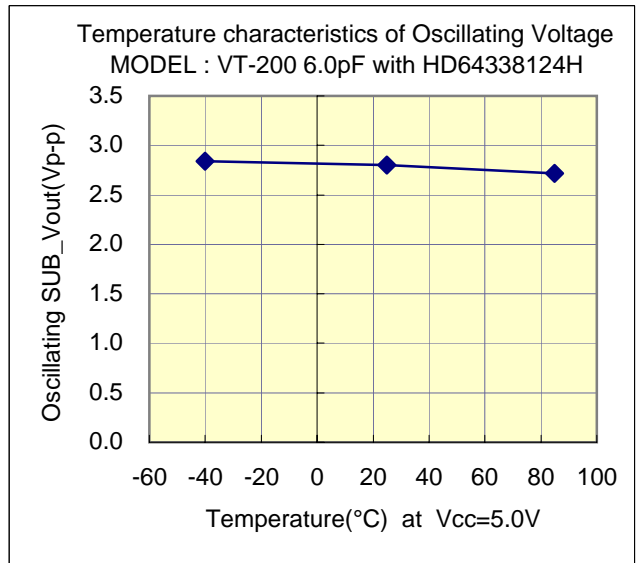
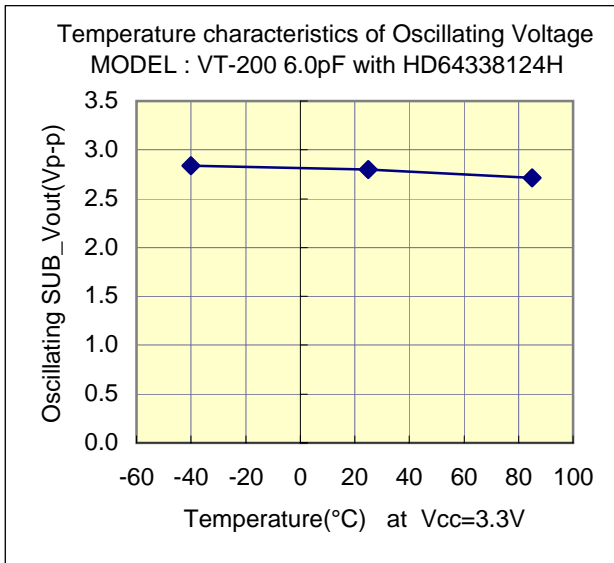
Evaluation of Subsystem Clock Oscillation Circuit

[HD64338124H-80A] QFP(14x14) 0.65mm pitch

Measurement conditions : 5.0V , 3.3V



Test Data : Temperature characteristics



Evaluation of Subsystem Clock Oscillation Circuit

[HD64338124H-80A] QFP(14x14) 0.65mm pitch

Measurement conditions : 5.0V , 3.3V



Referencial components layout(see Figure 1)

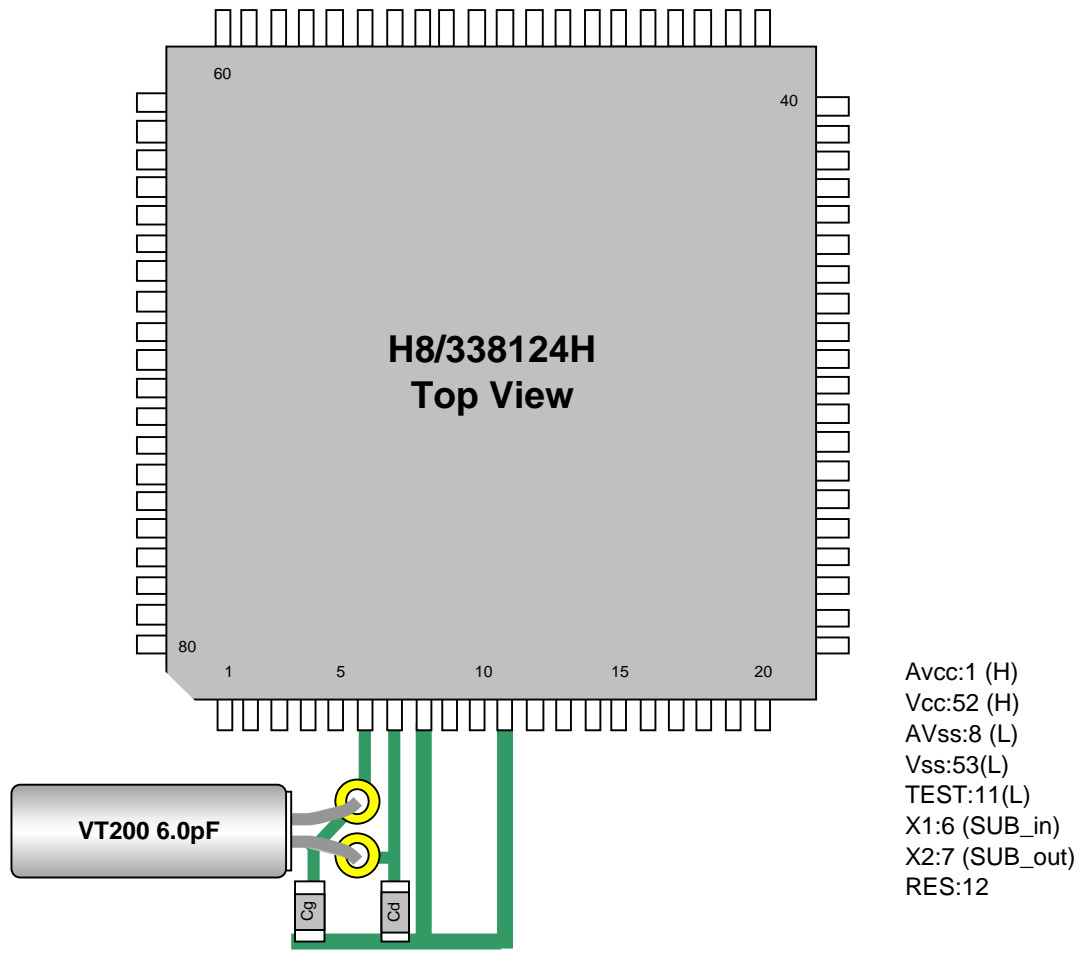


Figure 1 Referencial components layout

Notes Board Design

When using a crystal resonator, place the resonator and its load capacitors as close as possible to SUB_in and SUB_out pins.

Other signal lines should be routed away from the resonator circuit to prevent induction from interfering with correct oscillation (see figure 2).

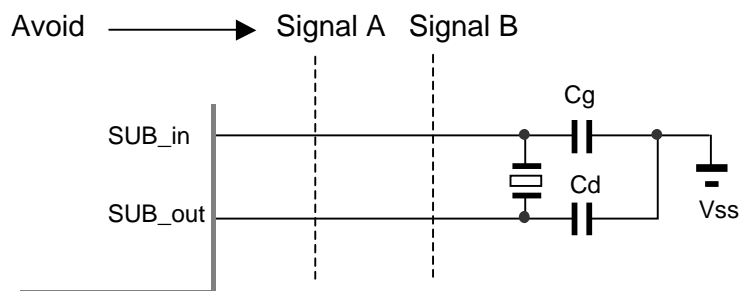


Figure 2 Example of Incorrect Board Design

Evaluation of Subsystem Clock Oscillation Circuit

[HD64338124H-80A] QFP(14x14) 0.65mm pitch

Measurement conditions : 5.0V , 3.3V



[Evaluation Sample : VT-200 6.0pF at 25°C]

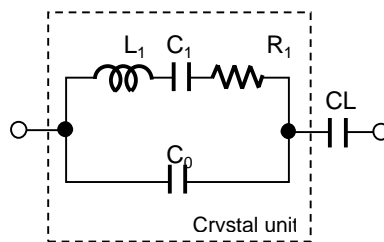
SAMPLE	No.	CL (pF)	Fo (Hz)	fr (Hz)	R1 (kohm)	Co (pF)	C1 (fF)	Q (k)
VT-200 6.0pF	1	6	32768.18	32762.98	28.7	0.91	2.193	77.2
	2	6	32768.18	32763.04	27.8	0.89	2.161	80.9
	3	6	32768.19	32763.00	27.2	0.90	2.187	81.7

[IC Test Data : IC samples Rd=0 ohm,Cg=7pF,Cd=7pF at 25°C]

Vcc (V)	IC samples	Fosc (Hz)	df / f (x10 ⁻⁶)	DL(x10 ⁻⁶ W)	-RL (kohm)	Vstart(V)	Ts(sec)
5.0	W#17-022	32768.09	-2.75	0.03	1137	0.86	0.43
	W#17-023	32768.06	-3.67	0.03	1137	0.86	0.44
	W#17-024	32768.09	-2.84	0.03	1137	0.86	0.44
	W#17-025	32768.10	-2.60	0.03	1137	0.85	0.43
	W#17-026	32768.06	-3.70	0.03	1137	0.86	0.46
3.3	W#17-022	32768.09	-5.19	0.03	1137	0.86	0.46
	W#17-023	32768.06	-5.81	0.03	1137	0.86	0.46
	W#17-024	32768.09	-5.10	0.03	1137	0.86	0.45
	W#17-025	32768.10	-4.58	0.03	1137	0.85	0.46
	W#17-026	32768.06	-5.81	0.03	1137	0.86	0.49

Remak (see figure 3)

$$F_o = f_r \times \{ C_1 / (2 \times (C_o + C_L) + 1) \} \text{ (Hz)}$$



- Fo : Load resonance frequency
- fr : Resonance frequency
- R1 : Motional resistance
- C1 : Motional capacitance
- Co : Shunt capacitance
- CL : Load Capacitance

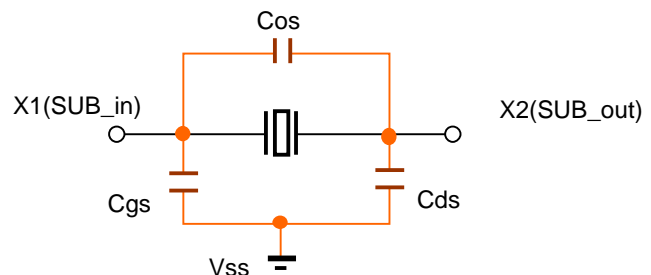
Figure 3 Equivalent circuit of crystal unit, and CL

Remak (see figure 4)

Approximate formula of the load capacitance of the circuit CL.

$$CL = C_g \times C_d / (C_g + C_d) + C_s \text{ (pF)}$$

Where Cs Stands for stray capacity of the circuit.



- Cos : X1_X2 Stray capacitance
- Cgs : X1_Vss Stray capacitance
- Cds : X2_Vss Stray capacitance

Figure 4 Stray capacitance Cos,Cgs,Cds of the circuit

Resonator circuit constants will differ depending on the resonator element, stray capacitance in its interconnecting circuit, and other factors. Suitable constants should be determined in consultation with the resonator element manufacturer.