

# Evaluation of Subsystem Clock Oscillation Circuit

[M38C59MCHP-80P] LQFP(12x12) 0.5mm pitch

Measurement conditions :3.3V, 5.0V

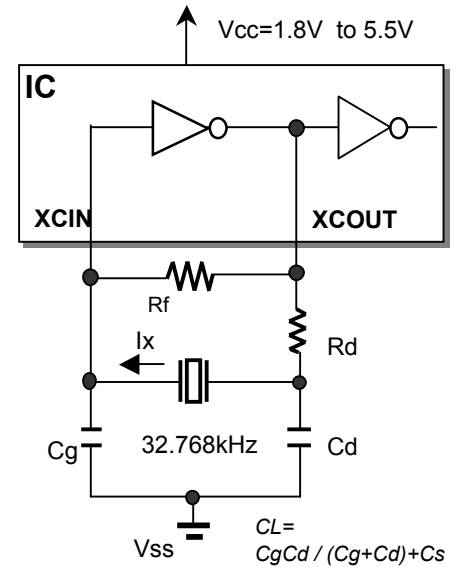
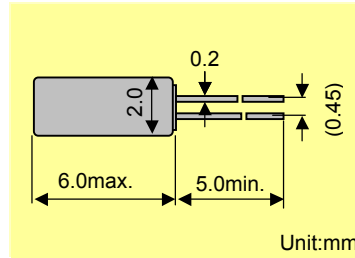


Model	:VT-200
Frequency	:Fo=32.768kHz
Frequency tolerance	:dF/Fo= +/-20x10 <sup>6</sup>
Load capacitance	:CL=12.5pF
Equivalent series resistance	:R1=50kohm max
Max. drive level	:DL=1x10 <sup>6</sup> W max
Level of drive	:DL=0.1x10 <sup>6</sup> W typ

## FEATURES

- 1.Compact tubular package
- 2.Photolithographic process
- 3.Excellent shock resistance and environmental characteristics.
- 4.Real time clocks, Timers, Portable applications

## DIMENSIONS(VT-200)



MODEL:VT-200 12.5pF with M38C59MCHP at 25°C

Key specifications	Vcc=3.3V	Vcc=5.0V	Remarks
Negative feedback resistance : Rf ( M ohm )	10	10	
Current control resistance : Rd ( k ohm )	100	100	Control drive level & secure phase margin
Capacitance at gate : Cg ( pF )	15	15	Optimal capacity in response to CL
Capacitance at drain : Cd ( pF )	18	18	( CL = Cd // Cg + stray capacitance )

Circuit characteristics ( at 25°C )	Vcc=3.3V	Vcc=5.0V	Remarks
Matching Accuracy : $df / f$ ( $\times 10^{-6}$ )	-1.2	0.9	Frequency offset volume at specified Vdd
Voltage Fluctuation : $+/-df / V$ ( $\times 10^{-6}$ )	0.6	0.5	Vdd +/-10% ( Standard operating voltage range )
Drive Level : DL ( $\times 10^{-6}$ W )	0.21	0.21	$DL=I_x^2 R_e < 1 \times 10^{-6}$ W, $R_e=R_1( 1 + C_o / CL )^2$
Negative resistance : $ -RL $ ( kohm )	464	504	5 times larger than $R_{1MAX}$
Oscillation allowance : M ( times )	9.3	10.1	Judgemental standard of oscillation stability
Voltage of oscillation start : Vstrat ( V )	1.69	1.69	
Voltage of oscillation stop : Vstop ( V )	1.44	1.44	
Oscillation start up time : Ts ( sec )	0.42	0.39	Time to reach 90% of output level

Temperature characteristics of circuit		Vcc=3.3V	Vcc=5.0V	Remarks
at -20°C	Variation : $df / T$ ( $\times 10^{-6}$ )	-66	-66	Typ.Tp=25°C ( K = $-3.5 \times 10^{-8} / ^\circ C^2$ )
at +85°C	Variation : $df / T$ ( $\times 10^{-6}$ )	-135	-134	Typ.Tp=25°C ( K = $-3.5 \times 10^{-8} / ^\circ C^2$ )

The above mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics. Please review and check above parameters at customer's end.

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We value the "takumi" spirit.

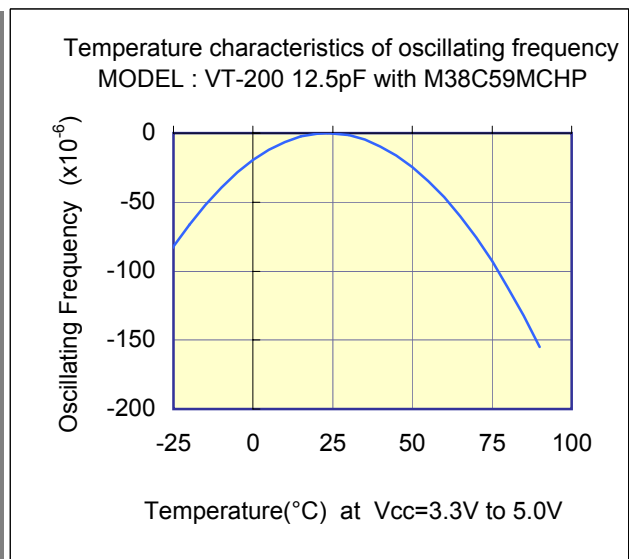
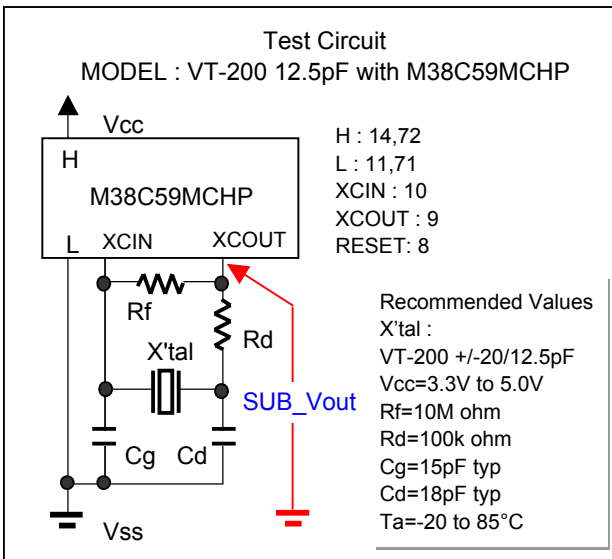
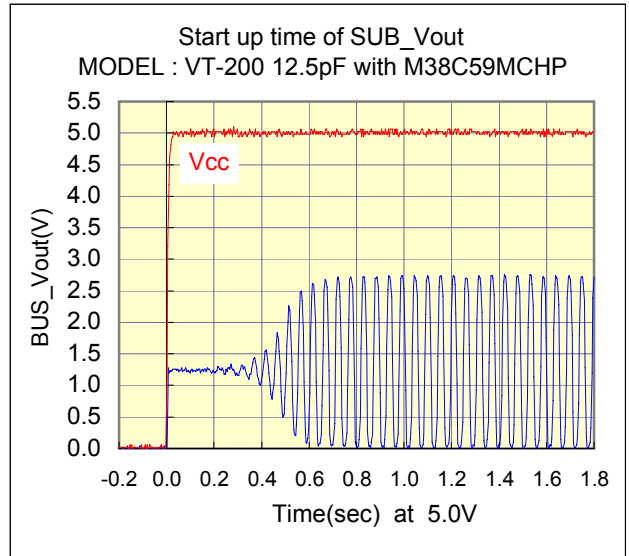
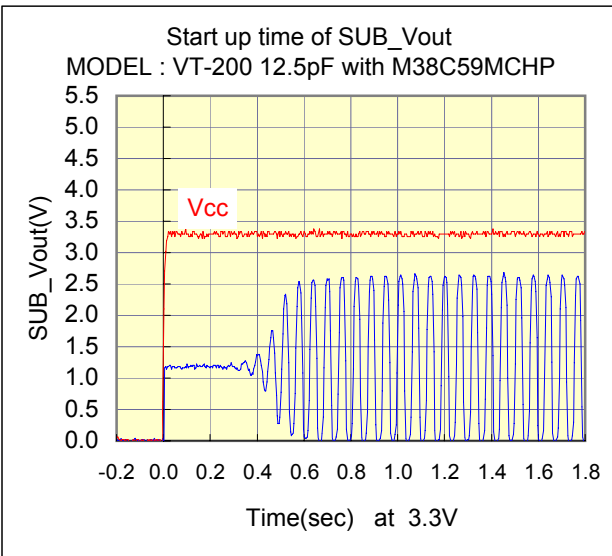
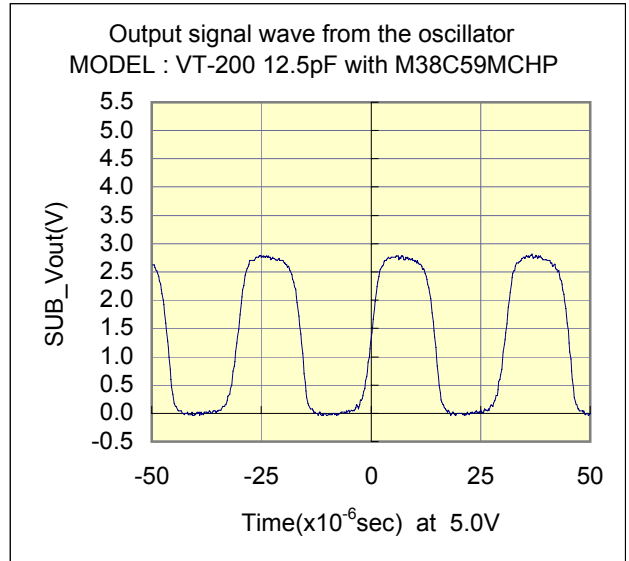
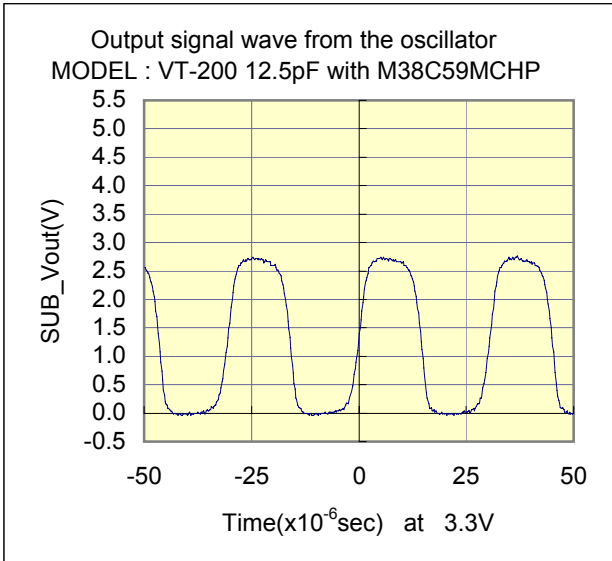
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## Test Data at 25°C

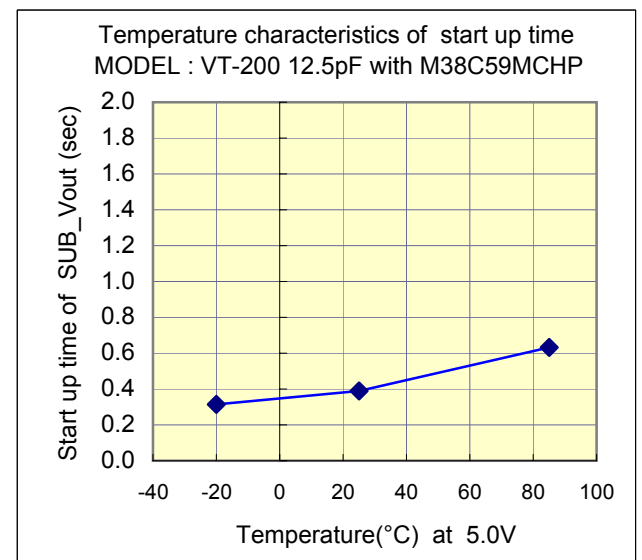
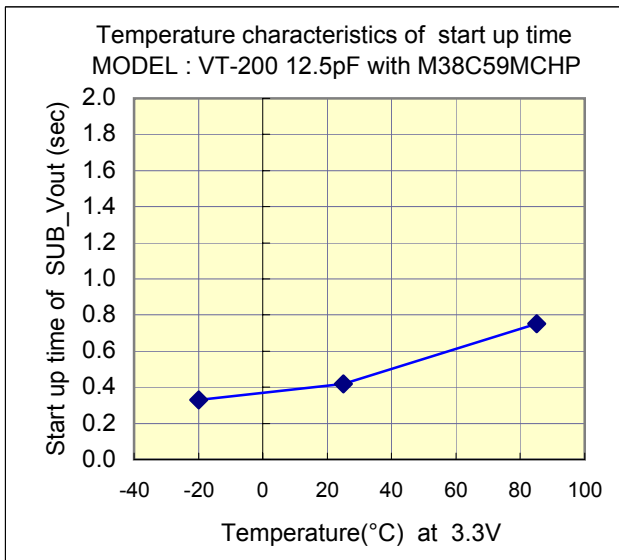
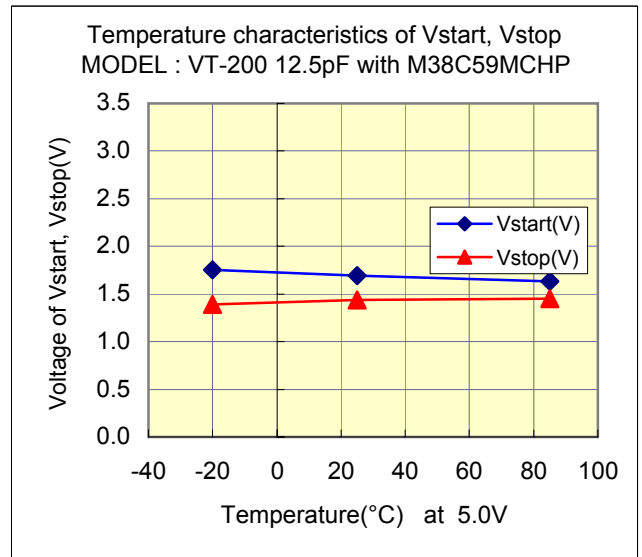
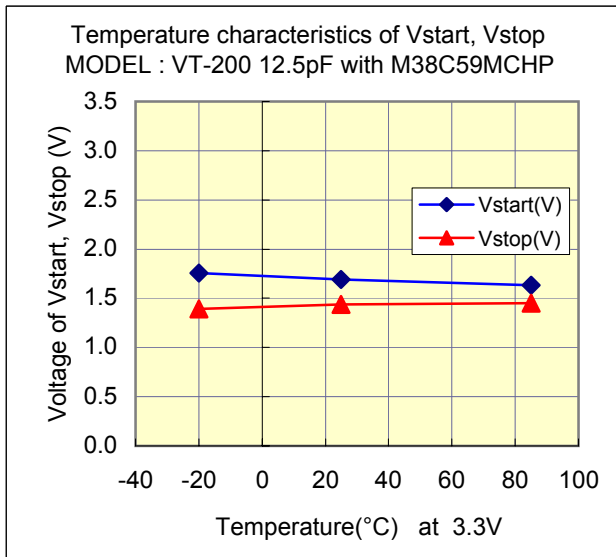
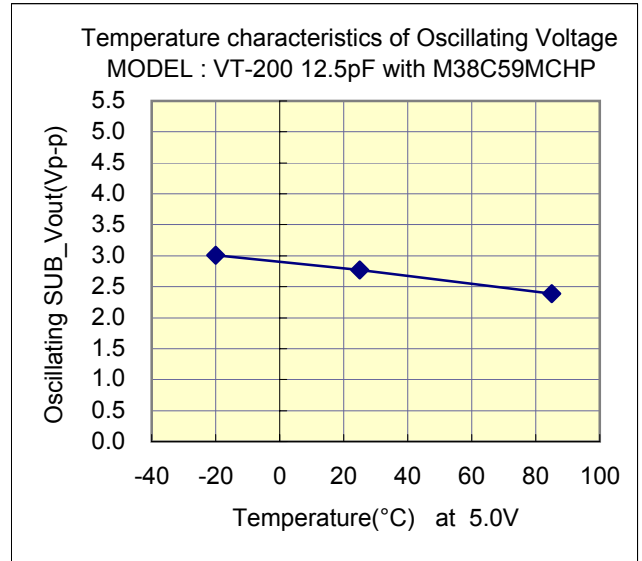
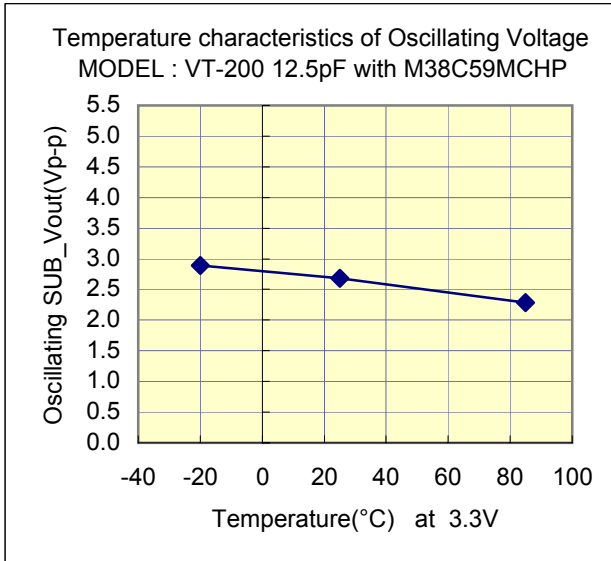


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## Test Data : Temperature characteristics

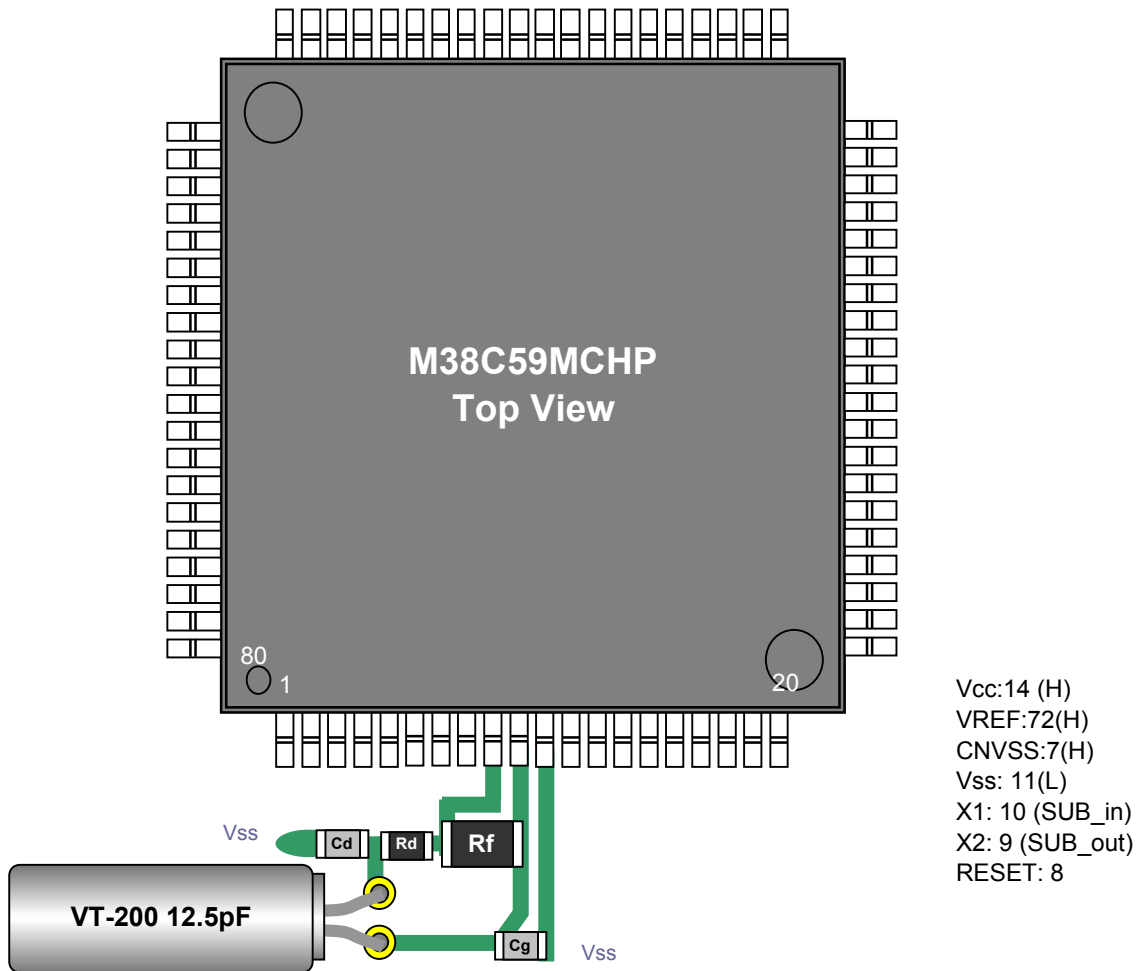


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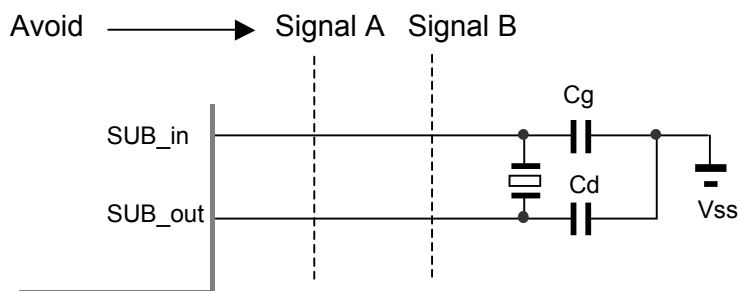
## Referential components layout(see Figure 1)



**Figure 1 Referential components layout**

## Notes for Board Design

When using a crystal resonator, place the resonator and its load capacitors as close as possible to SUB\_in and SUB\_out pins.  
 Other signal lines should be routed away from the resonator circuit to prevent induction from interfering with correct oscillation (see figure 2).



**Figure 2 Example of Incorrect Board Design**

**Remark** When using the subsystem clock, insert resistors Rd in series on the SUB\_out side.

# Evaluation of Subsystem Clock Oscillation Circuit



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Measurement conditions :3.3V, 5.0V

## [Evaluation Sample : VT-200 12.5pF at 25°C]

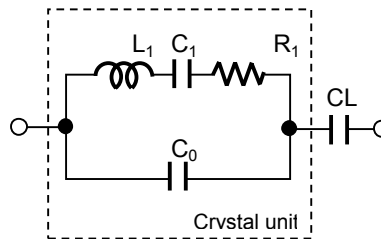
SAMPLE	No.	CL( pF )	Fo( Hz )	fr( Hz )	R1( kohm )	Co( pF )	C1( fF )	Q( k )
VT-200 12.5pF	1	12.5	32768.11	32765.28	27.4	0.91	2.319	76.5
	2	12.5	32768.09	32765.24	26.9	0.89	2.333	77.4
	3	12.5	32768.34	32765.45	29.9	0.93	2.368	68.6

## [IC Test Data : IC samples Rf=10M,Rd=100k ohm,Cg=15pF,Cd=18pF at 25°C]

Vcc(V)	IC samples	Fosc( Hz )	df / f( x10 <sup>-6</sup> )	DL(x10 <sup>-6</sup> W)	-RL  ( kohm )	Vstart( V )	Ts(sec)
5.0	TYP	32768.368	0.88	0.21	504	1.69	0.39
	HH	32768.380	1.25	0.25	504	1.85	0.37
	HL	32768.327	-0.37	0.22	464	1.81	0.39
	LH	32768.395	1.70	0.21	504	1.58	0.40
	LL	32768.306	-1.01	0.17	464	1.55	0.40
3.3	TYP	32768.300	-1.20	0.21	464	1.69	0.42
	HH	32768.260	-2.42	0.25	464	1.85	0.38
	HL	32768.251	-2.69	0.22	464	1.81	0.44
	LH	32768.327	-0.37	0.21	504	1.58	0.40
	LL	32768.236	-3.15	0.17	464	1.55	0.43

### Remark ( see figure 3 )

$$F_o = f_r \times \{ C_1 / ( 2 \times ( C_o + C_L ) + 1 ) \} \text{ ( Hz )}$$



- Fo : Load resonance frequency
- fr : Resonance frequency
- R1 : Motional resistance
- C1 : Motional capacitance
- Co : Shunt capacitance
- CL : Load Capacitance

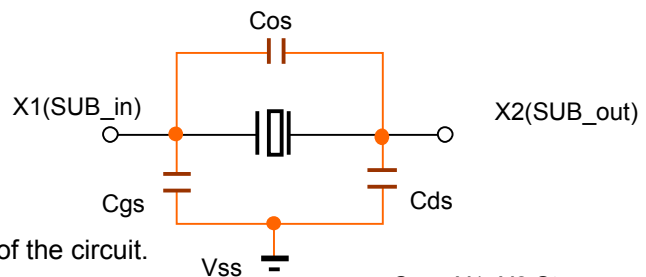
Figure 3 Equivalent circuit of crystal unit, and CL

### Remark ( see figure 4 )

Approximate formula of the load capacitance of the circuit CL.

$$CL = C_g \times C_d / ( C_g + C_d ) + C_s \text{ ( pF )}$$

Where Cs(=2 to 4pF) Stands for stray capacitance of the circuit.



- Cos : X1\_X2 Stray capacitance
- Cgs : X1\_Vss Stray capacitance
- Cds : X2\_Vss Stray capacitance

Figure 4 Stray capacitance Cos,Cgs,Cds of the circuit

Resonator circuit constants will differ depending on the resonator element, stray capacitance in its interconnecting circuit, and other factors. Suitable constants should be determined in consultation with the resonator element manufacturer.