

Evaluation of Subsystem Clock Oscillation Circuit

[M3823AGFFP-80P] PQFP(14x20) 0.80mm pitch

Measurement conditions :3.3V, 5.0V

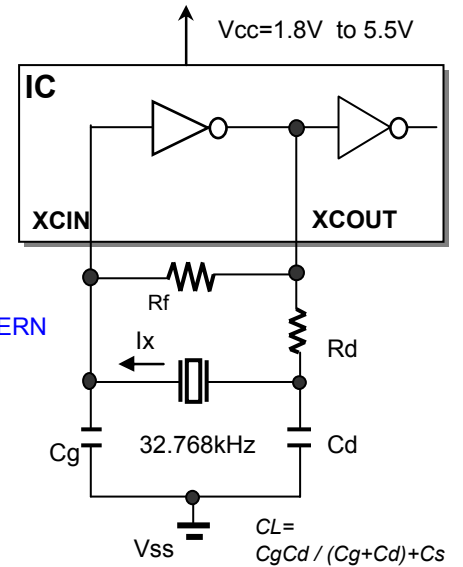
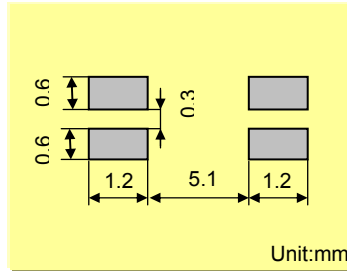


Model	:SSP-T7
Frequency	:Fo=32.768kHz
Frequency tolerance	:dF/Fo= +/-20x10 ⁶
Load capacitance	:CL=12.5pF
Equivalent series resistance	:R1=65kohm max
Max. drive level	:DL=1x10 ⁶ W max
Level of drive	:DL=0.1x10 ⁶ W typ

FEATURES

- 1.Ultra thin type with 1.4mm Max.
- 2.SMD type suitable for automatic & high density surface mounting.
- 3.Plastic mold package containing highly reliable tubular type quartz crystal.
- 4.Excellent shock and heat resistance.
- 5.Cellular phones,PDA,Radio communication equipment, Portable applications etc.

RECOMMENDED SOLDERING PATTERN



Remark) I_x : current through crystal

MODEL:SSP-T7 12.5pF with M3823AGFFP at 25°C

Key specifications	Vcc=3.3V	Vcc=5.0V	Remarks
Negative feedback resistance : R_f (M ohm)	10	10	
Current control resistance : R_d (k ohm)	100	100	Control drive level & secure phase margin
Capacitance at gate : C_g (pF)	18	18	Optimal capacity in response to CL
Capacitance at drain : C_d (pF)	18	18	($CL = C_d // C_g + \text{stray capacitance}$)

Circuit characteristics (at 25°C)	Vcc=3.3V	Vcc=5.0V	Remarks
Matching Accuracy : df / f ($\times 10^{-6}$)	-0.3	1.5	Frequency offset volume at specified Vdd
Voltage Fluctuation : $\pm df / V$ ($\times 10^{-6}$)	0.5	0.5	Vdd +/-10% (Standard operating voltage range)
Drive Level : DL ($\times 10^{-6}$ W)	0.30	0.30	$DL = I_x^2 R_e < 1 \times 10^{-6}$ W, $R_e = R_1(1 + C_o / CL)^2$
Negative resistance : $ -RL $ (kohm)	731	801	5 times larger than R_{1MAX}
Oscillation allowance : M (times)	11.3	12.3	Judgemental standard of oscillation stability
Voltage of oscillation start : V_{strat} (V)	1.71	1.71	
Voltage of oscillation stop : V_{stop} (V)	1.38	1.38	
Oscillation start up time : T_s (sec)	0.47	0.46	Time to reach 90% of output level

Temperature characteristics of circuit		Vcc=3.3V	Vcc=5.0V	Remarks
at -20°C	Variation : df / T ($\times 10^{-6}$)	-60	-60	Typ.Tp=25°C ($K = -3.5 \times 10^{-8} / ^\circ C^2$)
at +85°C	Variation : df / T ($\times 10^{-6}$)	-131	-131	Typ.Tp=25°C ($K = -3.5 \times 10^{-8} / ^\circ C^2$)

The above mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics. Please review and check above parameters at customer's end.

Seiko Instruments USA Inc.

2990, West Lomita Blvd., Torrance, CA 90505, U.S.A
 Telephone : +1 310-517-7771 Facsimile : +1 310-517-7792
 Email : crystals@siu-la.com

Seiko Instruments GmbH

Siemensstrasse 9, D-63263 Neu-Isenburg, Germany
 Telephone : +49-6102-297-0 Facsimile : +49-6102-297-320
 Email : info@seiko-instruments.de

Seiko Instruments Inc.

1-8, Nakase, Mihama-ku, Chiba-shi, Chiba 261-8507, Japan
 Facsimile : +81-43-211-8030
 E-mail : component@sii.co.jp



We value the "takumi" spirit.

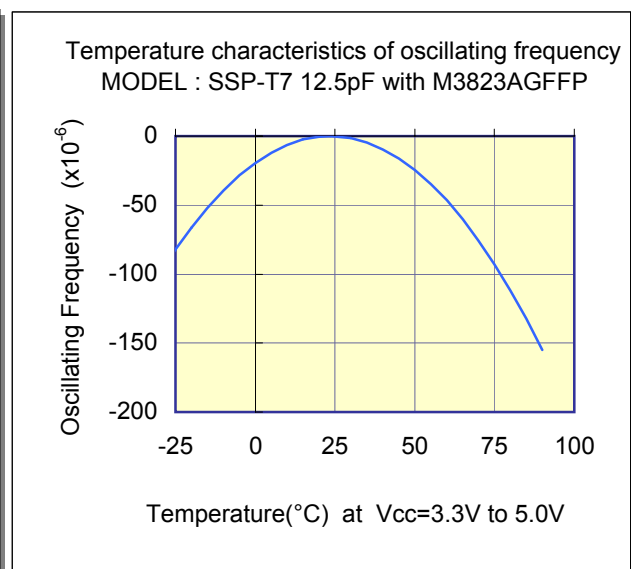
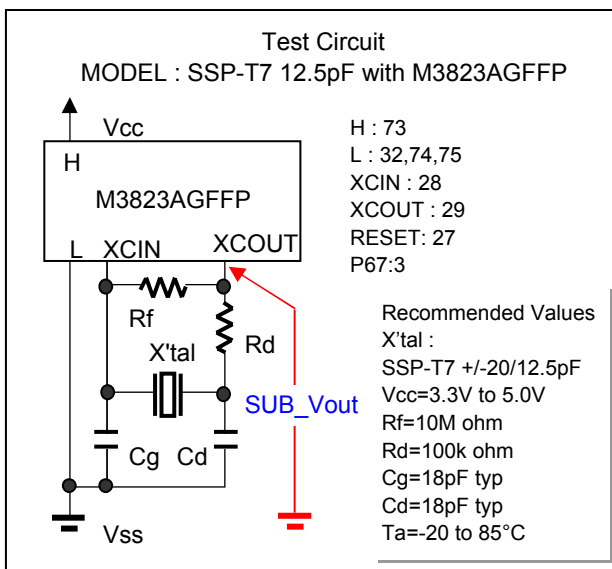
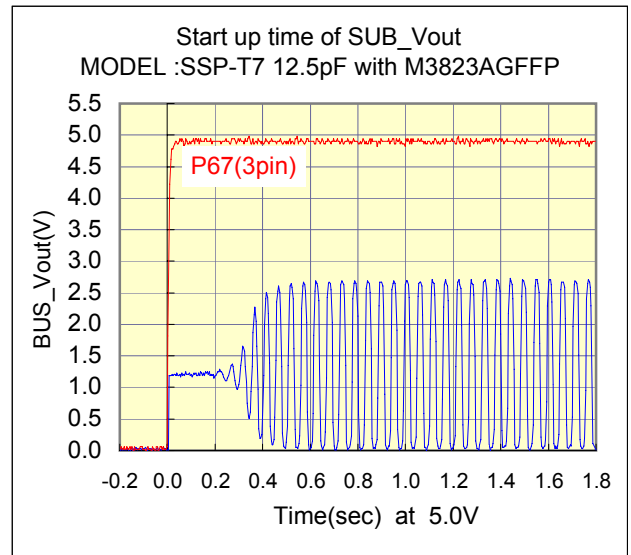
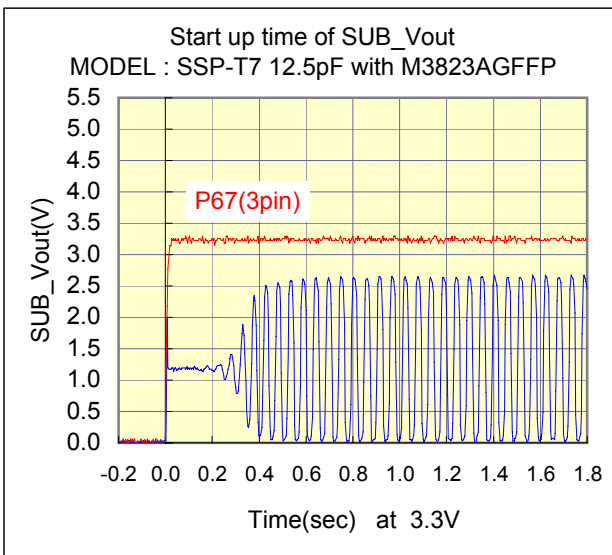
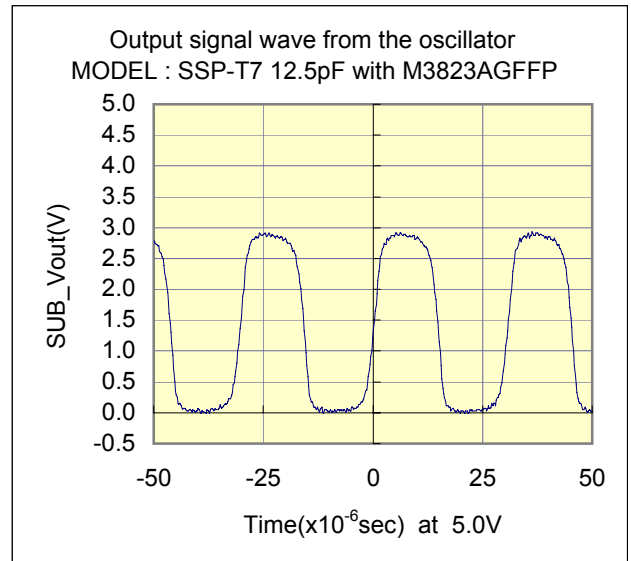
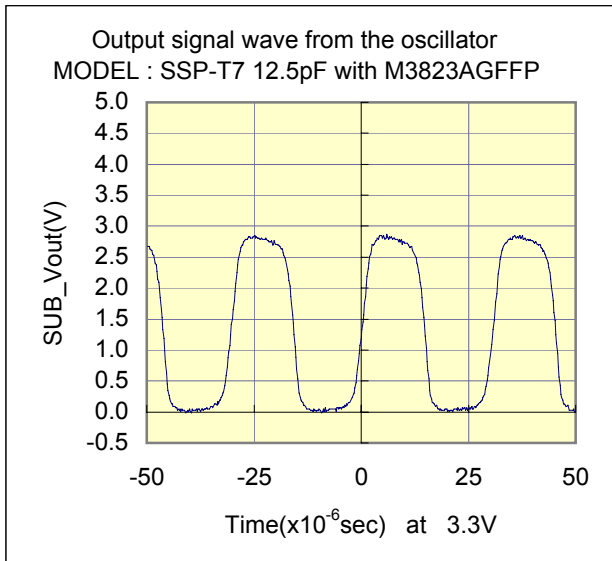
Seiko Instruments Inc.
 Phone: +81-43-211-1207(Direct)

Evaluation of Subsystem Clock Oscillation Circuit

[M3823AGFFP-80P] PQFP(14x20) 0.80mm pitch
 Measurement conditions :3.3V, 5.0V



Test Data at 25°C



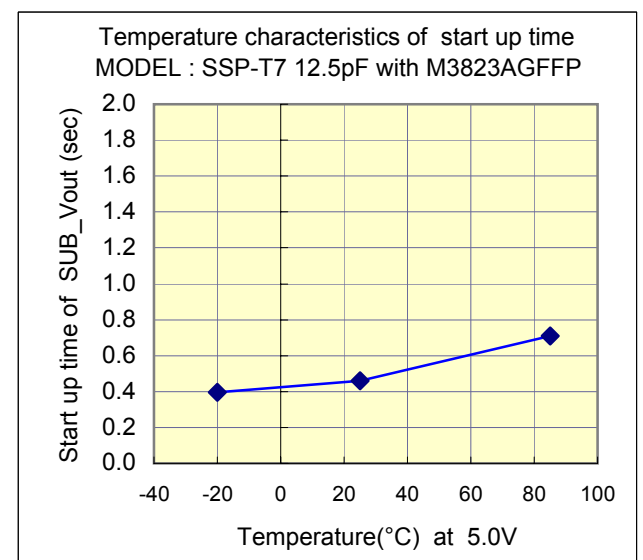
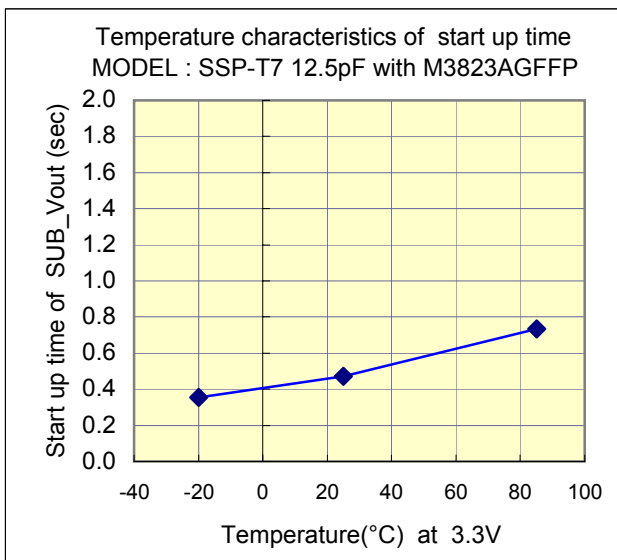
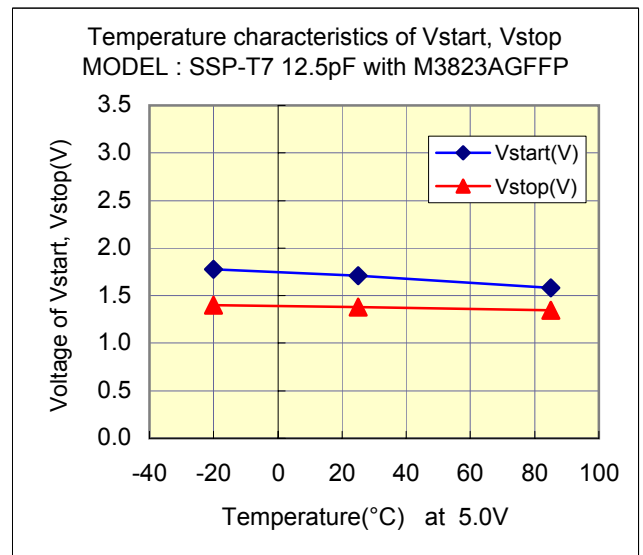
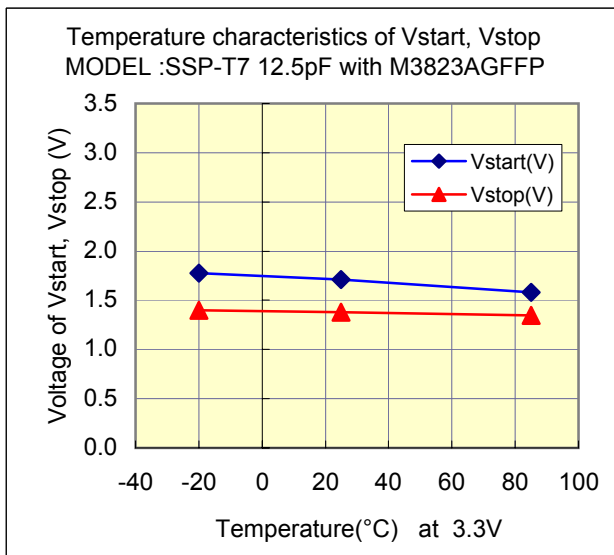
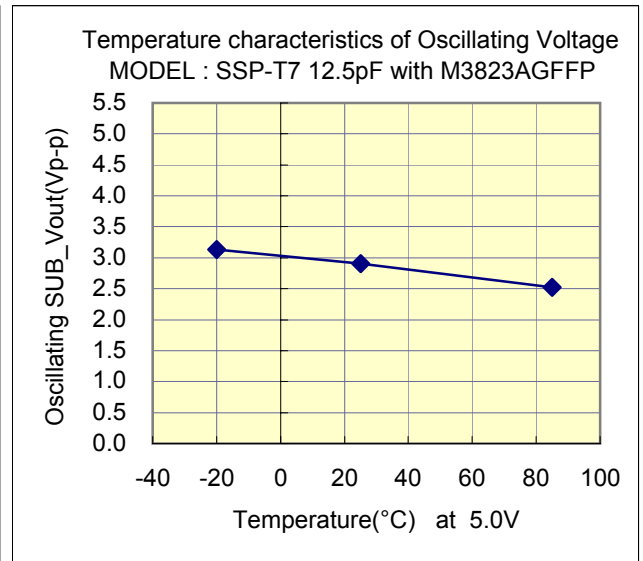
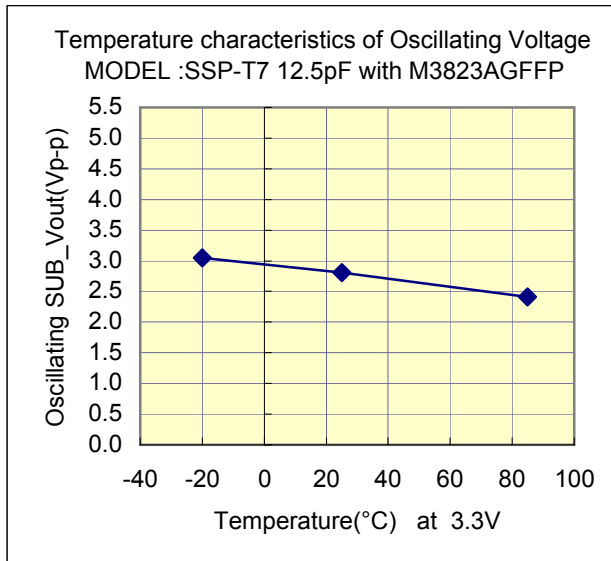
Evaluation of Subsystem Clock Oscillation Circuit

[M3823AGFFP-80P] PQFP(14x20) 0.80mm pitch

Measurement conditions :3.3V, 5.0V



Test Data : Temperature characteristics



Evaluation of Subsystem Clock Oscillation Circuit

[M3823AGFFP-80P] PQFP(14x20) 0.80mm pitch

Measurement conditions :3.3V, 5.0V



Referential components layout(see Figure 1)

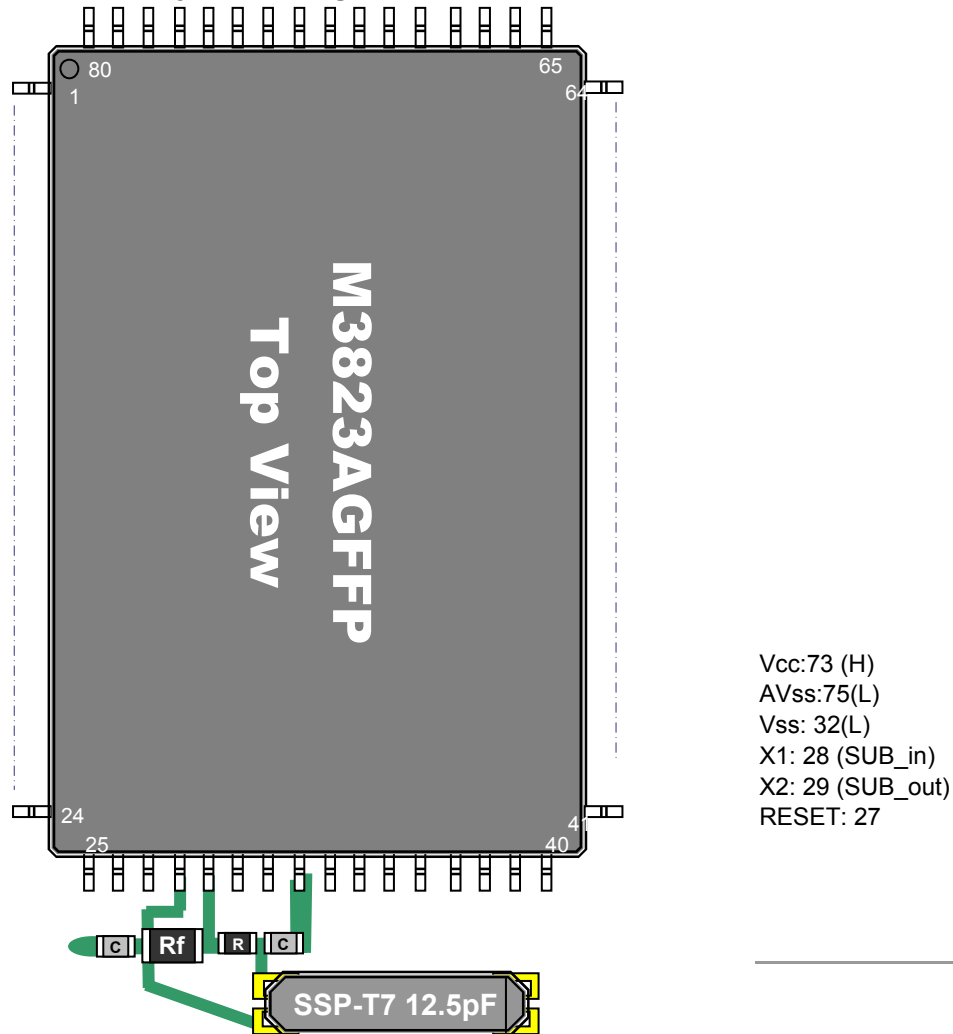


Figure 1 Referential components layout

Notes for Board Design

When using a crystal resonator, place the resonator and its load capacitors as close as possible to SUB_in and SUB_out pins.

Other signal lines should be routed away from the resonator circuit to prevent induction from interfering with correct oscillation (see figure 2).

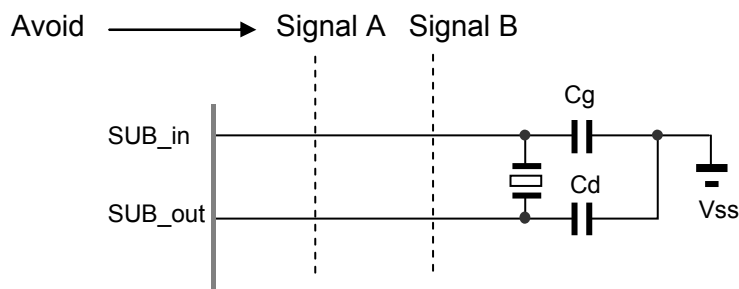


Figure 2 Example of Incorrect Board Design

Remark When using the subsystem clock, insert resistors R_d in series on the SUB_out side.

Evaluation of Subsystem Clock Oscillation Circuit

[M3823AGFFP-80P] PQFP(14x20) 0.80mm pitch

Measurement conditions :3.3V, 5.0V



[Evaluation Sample : SSP-T7 12.5pF at 25°C]

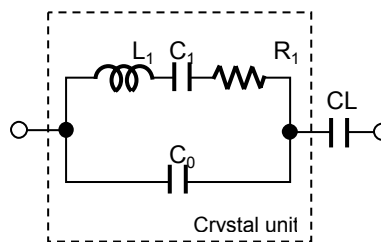
SAMPLE	No.	CL(pF)	Fo(Hz)	fr(Hz)	R1(kohm)	Co(pF)	C1(fF)	Q(k)
SSP-T7 12.5pF	1	12.5	32768.14	32765.66	38.0	0.88	2.025	63.2
	2	12.5	32768.06	32765.60	45.1	0.84	2.003	53.8
	3	12.5	32768.16	32765.70	39.8	0.85	2.004	60.9

[IC Test Data : IC samples Rf=10M,Rd=100k ohm,Cg=18pF,Cd=18pF at 25°C]

Vcc(V)	IC samples	Fosc(Hz)	df / f(x10 ⁻⁶)	DL(x10 ⁻⁶ W)	-RL (kohm)	Vstart(V)	Ts(sec)
5.5	TYP	32768.110	1.53	0.30	801	1.71	0.46
	HH	32768.076	0.49	0.35	871	1.82	0.40
	HL	32768.030	-0.92	0.29	801	1.70	0.44
	LH	32768.110	1.53	0.29	871	1.61	0.39
	LL	32768.028	-0.98	0.24	731	1.51	0.44
3.3	TYP	32768.050	-0.31	0.30	731	1.71	0.47
	HH	32768.010	-1.53	0.35	801	1.82	0.44
	HL	32767.960	-3.05	0.29	731	1.70	0.46
	LH	32768.057	-0.09	0.29	801	1.61	0.41
	LL	32767.968	-2.81	0.23	671	1.51	0.49

Remark (see figure 3)

$$F_o = f_r \times \left\{ \frac{C_1}{2 \times (C_o + C_L) + 1} \right\} \text{ (Hz)}$$



Fo : Load resonance frequency
 fr : Resonance frequency
 R1 : Motional resistance
 C1 : Motional capacitance
 Co : Shunt capacitance
 CL : Load Capacitance

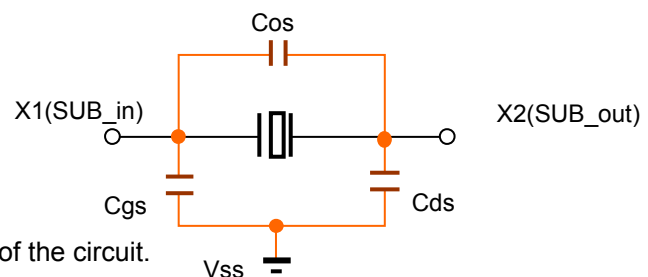
Figure 3 Equivalent circuit of crystal unit, and CL

Remark (see figure 4)

Approximate formula of the load capacitance of the circuit CL.

$$CL = \frac{C_g \times C_d}{C_g + C_d} + C_s \text{ (pF)}$$

Where Cs(=2 to 4pF) Stands for stray capacitance of the circuit.



Cos : X1_X2 Stray capacitance
 Cgs : X1_Vss Stray capacitance
 Cds : X2_Vss Stray capacitance

Figure 4 Stray capacitance Cos,Cgs,Cds of the circuit

Resonator circuit constants will differ depending on the resonator element, stray capacitance in its interconnecting circuit, and other factors. Suitable constants should be determined in consultation with the resonator element manufacturer.