

Evaluation of a Low Frequency Clock Oscillation Circuit

VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : 3.0V



New

VT-200-FL

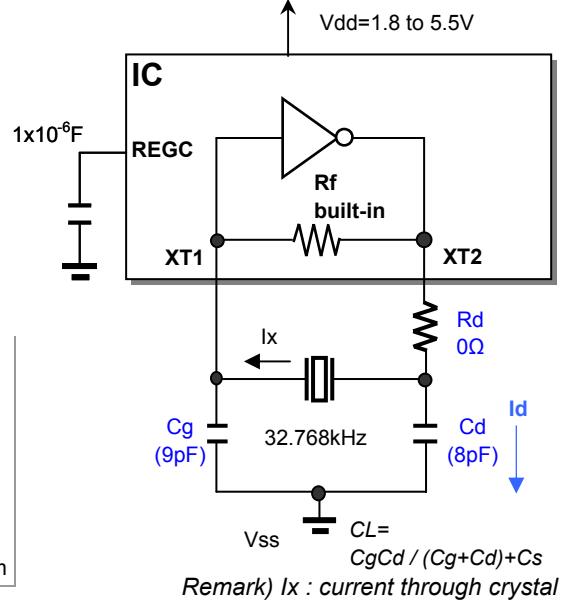
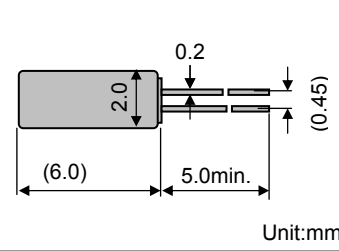


Model :VT-200-FL
 Frequency :Fo=32.768kHz
 Frequency tolerance :dF/Fo= +/-20x10⁻⁶
 Load capacitance :CL=6.0pF
 Equivalent series resistance :R1=50kΩ max
 Max. drive level :DL=1μW max
 Level of drive :DL=0.01μW typ

FEATURES

- 1.Compact tubular package
- 2.Photolithographic process
- 3.Excellent shock resistance and environmental characteristics.
- 4.Real time clocks, Timers, Portable applications

DIMENSIONS(VT-200)



	REGVDD	Oscillation mode
Mode 1	2.4V	Low (*1)
Mode 2	1.8V	

Low power consumption 78K0R/Kx3-L and VT-200-FL 6.0pF

***1 ; Low current consumption mode**

MODEL:VT-200-FL 6.0pF with uPD78F1014GC at 25°C

Key specifications	mode 2	mode 1	Remarks
Current control resistance : Rd (kΩ)	0	0	Control drive level & secure phase margin
Capacitance at gate : Cg (pF)	9	9	Optimal capacitance in response to CL
Capacitance at drain : Cd (pF)	8	8	(CL = Cd // Cg + stray capacitance)

Circuit characteristics (at 25°C)	mode 2	mode 1	Remarks
Matching Accuracy : df / f (x10 ⁻⁶)	0.9	1.1	Frequency offset volume at specified Vdd
Voltage Fluctuation : +/-df / V (x10 ⁻⁶)	0.0	0.0	Vdd +/-10% (Standard operating voltage range)
Drive Level : DL (μW)	0.01	0.01	DL=Ix ² Re < 1x10 ⁻⁶ W, Re=R1(1 + Co / CL) ²
Negative resistance : - RL (kΩ)	516	516	5 times larger than R _{1MAX}
Oscillation allowance : M (times)	10	10	Judgmental standard of oscillation stability
Low current consumption : Id (nA)	213	213	Cd charge current, Id = ωCd*Vd < 250nA
Voltage of oscillation start : Vstrat (V)	1.72	1.61	
Voltage of oscillation stop : Vstop (V)	1.59	1.59	
Oscillation start up time : Ts (sec)	1.05	1.05	Time to reach 90% of output level, Ts < 2.0sec

Temperature characteristics of circuit		mode 2	mode 1	Remarks
at -40°C	Variation : df / T (x10 ⁻⁶)	-142	-142	Typ.Tp=25°C (K = -3.5x10 ⁻⁸ / °C ²)
at +85°C	Variation : df / T (x10 ⁻⁶)	-124	-124	Typ.Tp=25°C (K = -3.5x10 ⁻⁸ / °C ²)

The above mentioned value is only for your reference. The value is for the arbitrary samples and does not guarantee the product's characteristics. Please review and check above parameters at customer's end.

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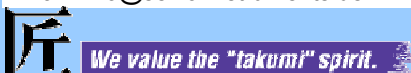
2990,West Lomita Blvd., Torrance, CA 90505, U.S.A
 Telephone :+1 310-517-7771 Facsimile :+1 310-517-7792
 Email :info@siu-la.com

Seiko Instruments GmbH

Siemensstrasse 9,D-63263 Neu-Isenburg,Germany
 Telephone :+49-6102-297-0 Facsimile :+49-6102-297-320
 Email :info@seiko-instruments.de

Seiko Instruments Inc.

1-8,Nakase,Mihama-ku,Chiba-shi,Chiba 261-8507,Japan
 Facsimile :+81-43-211-8030
 E-mail :component@sii.co.jp



Seiko Instruments Inc.
 Phone:+81-43-211-1207(Direct)

Evaluation of a Low Frequency Clock Oscillation Circuit

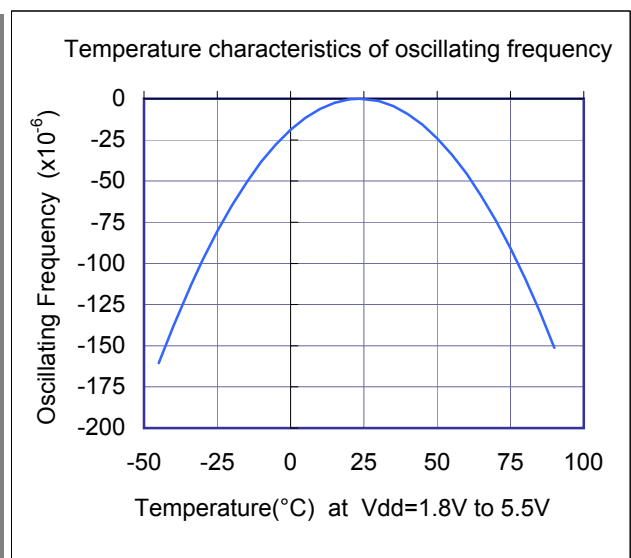
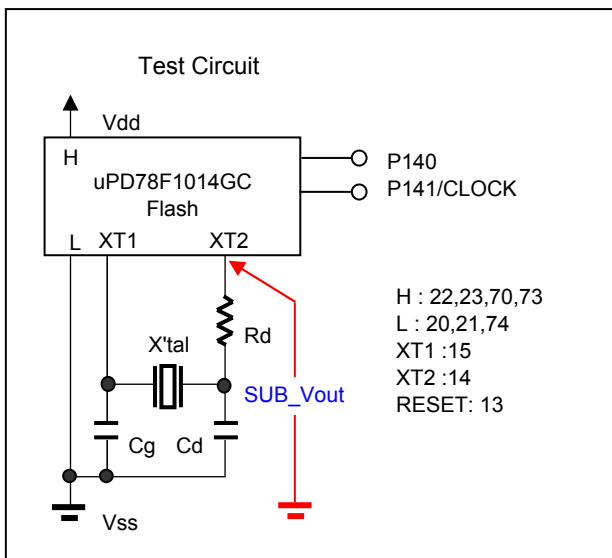
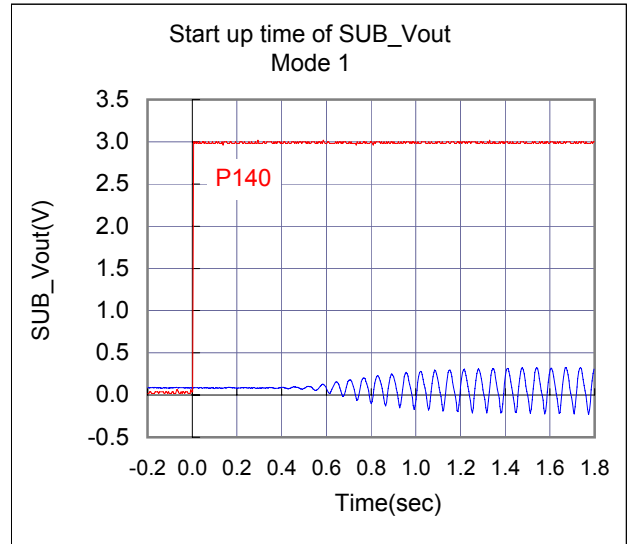
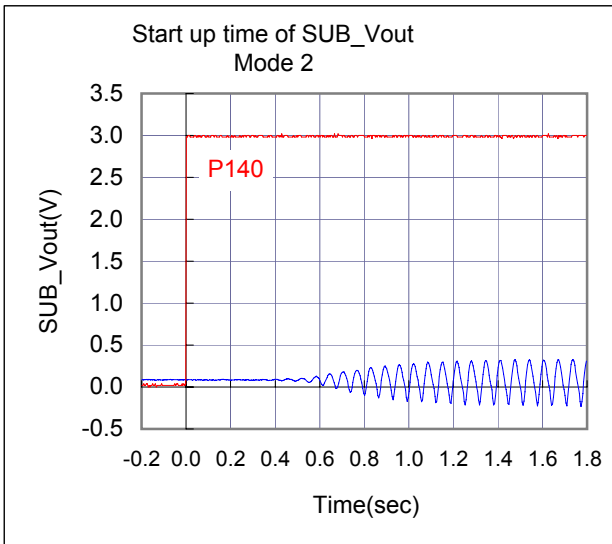
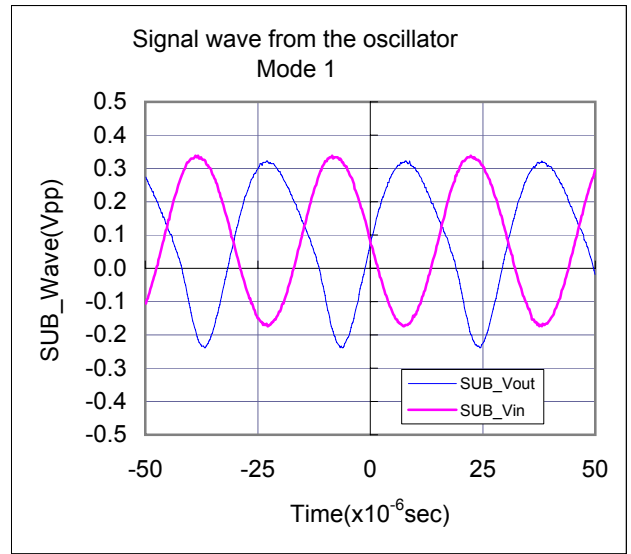
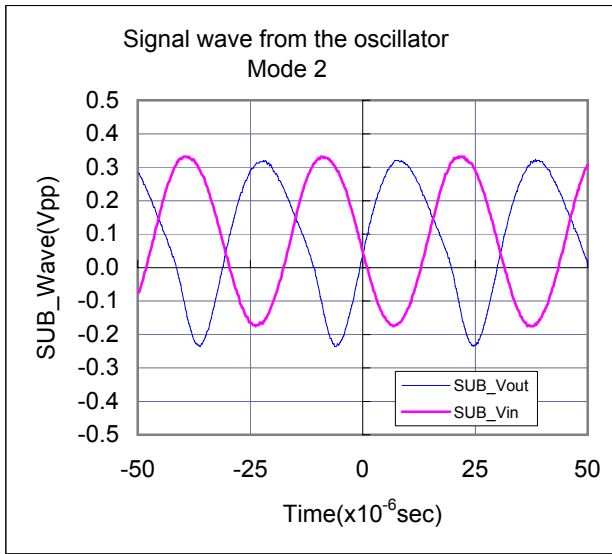
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : 3.0V



Low current consumption mode

Test Data at 25°C



Evaluation of a Low Frequency Clock Oscillation Circuit

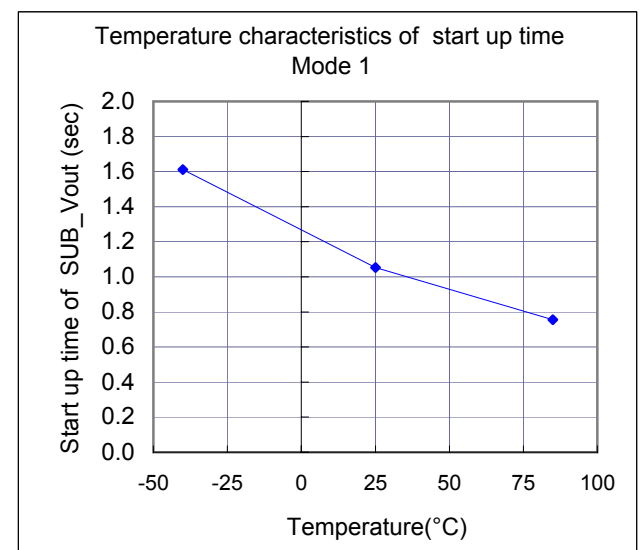
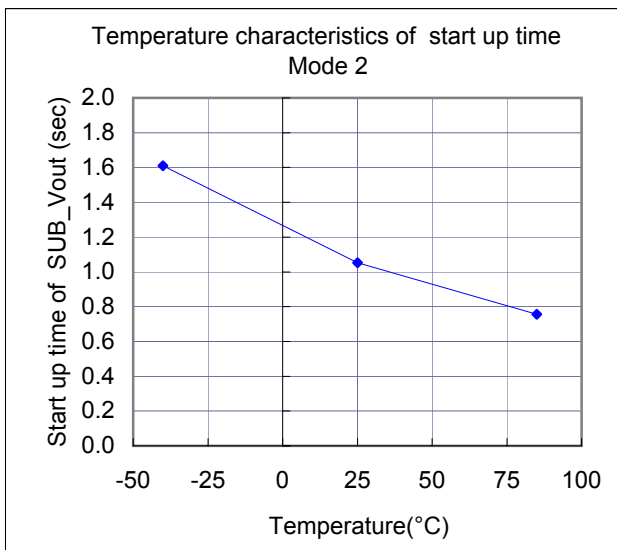
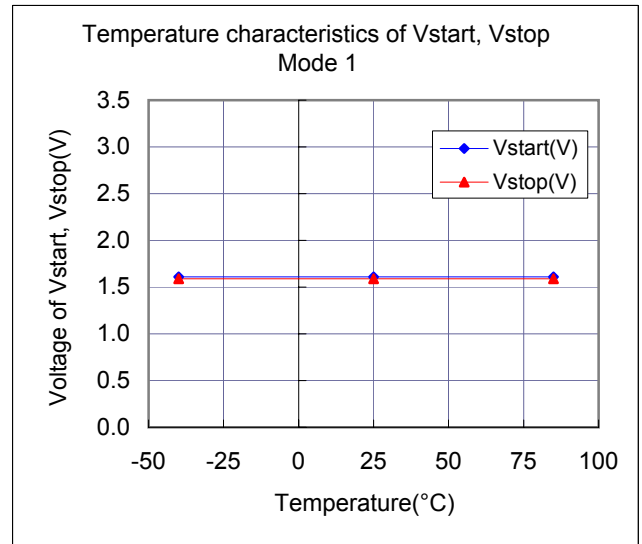
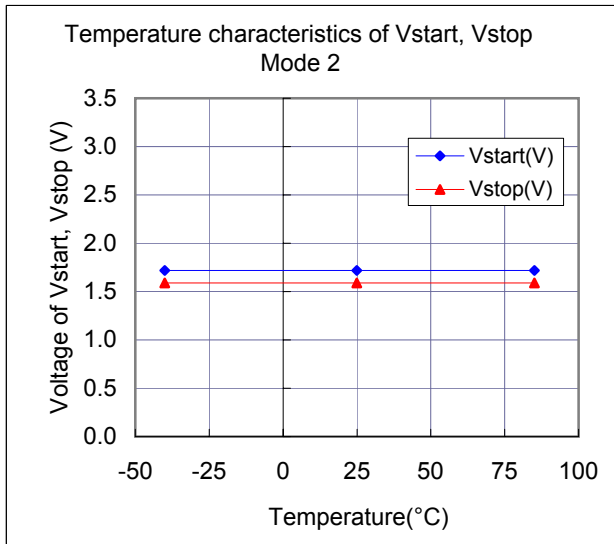
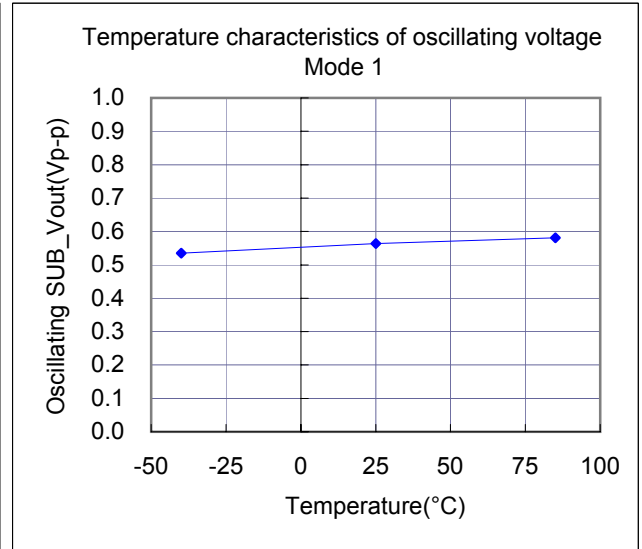
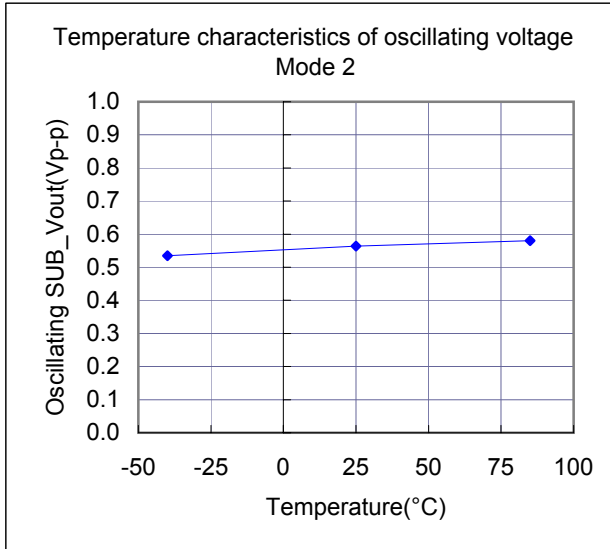
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : 3.0V



Low current consumption mode

Test Data : Temperature characteristics



Evaluation of a Low Frequency Clock Oscillation Circuit

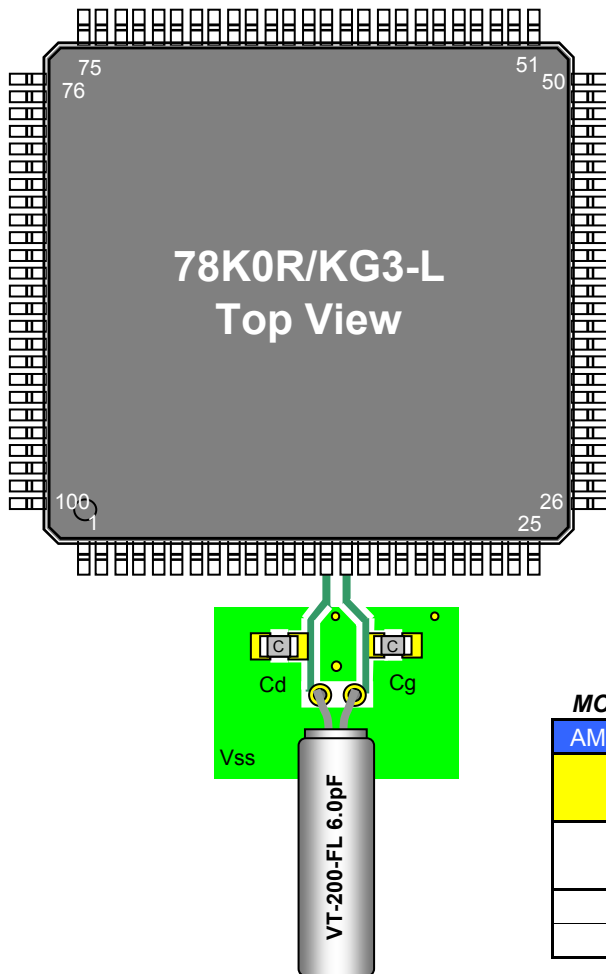
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : 3.0V



Low current consumption mode

Referential components layout(see Figure 1)



78K0R/Kx3-L series

*78K0R/KF3-L

uPD78F1010

uPD78F1011

uPD78F1012

*78K0R/KG3-L

uPD78F1013

uPD78F1014

MODEL:VT-200-FL 6.0pF with uPD78F1014GC at 25°C

AMPHS1	AMPHS0	Oscillation mode selection
0	0	Low consumption oscillation mode. (default) ; Id=210nA typ.
0	1	Normal consumption oscillation mode. Id=380nA typ.
1	0	Extremely low consumption oscillation mode. Id=120nA typ.
1	1	

Figure 1 Referential components layout

Notes for Board Design

When using a crystal resonator, place the resonator and its load capacitors as close as possible to SUB_in and SUB_out pins.

Other signal lines should be routed away from the resonator circuit to prevent induction from interfering with correct oscillation (see figure 2).

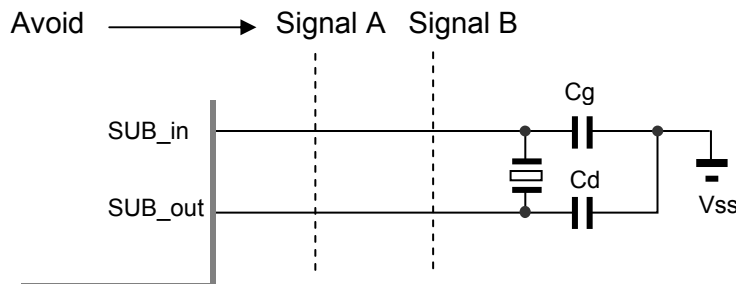


Figure 2 Example of Incorrect Board Design

Remark When using the subsystem clock, insert a resistor, Rd, in series on the SUB_out side.



Evaluation of a Low Frequency Clock Oscillation Circuit

VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : 3.0V



Low current consumption mode

[Evaluation Sample at 25°C]

SAMPLE	No.	CL(pF)	Fo(Hz)	fr(Hz)	R1(kΩ)	Co(pF)	C1(fF)	Q(k)
VT-200-FL	1	6.0	32768.02	32763.09	35.4	0.86	2.065	66.5
	2	6.0	32768.05	32763.10	34.8	0.85	2.068	67.5
	3	6.0	32768.11	32763.13	34.6	0.87	2.088	67.3

[IC Test Data : IC sample Rd=0Ω,Cg=9pF,Cd=8pF at 25°C]

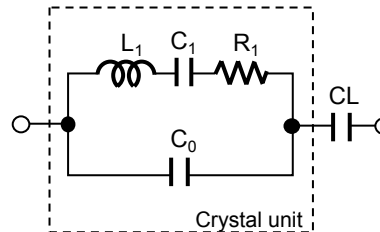
MODE	IC sample	Fosc(Hz)	df / f(x10 ⁻⁶)	DL(μW)	M(times)	XT2(Vpp)	Id(nA)	Vstart(V)	Ts(sec)
Mode 1	Sample 1	32768.07	1.4	0.02	9	0.58	220	1.58	1.19
	Sample 2	32768.06	1.1	0.01	10	0.54	204	1.58	1.17
	Sample 3	32768.06	1.1	0.01	10	0.56	213	1.61	1.05
	Sample 4	32768.06	1.2	0.01	11	0.59	223	1.59	0.93
	Sample 5	32768.05	0.9	0.01	10	0.54	205	1.59	0.94

[IC Test Data : IC sample Rd=0Ω,Cg=9pF,Cd=8pF at 25°C]

MODE	IC sample	Fosc(Hz)	df / f(x10 ⁻⁶)	DL(μW)	M(times)	XT2(Vpp)	Id(nA)	Vstart(V)	Ts(sec)
Mode 2	Sample 1	32768.06	1.2	0.02	9	0.58	220	1.67	1.19
	Sample 2	32768.05	0.8	0.01	10	0.54	204	1.68	1.17
	Sample 3	32768.05	0.9	0.01	10	0.56	213	1.72	1.05
	Sample 4	32768.05	1.0	0.01	11	0.59	223	1.69	0.93
	Sample 5	32768.04	0.7	0.01	10	0.54	206	1.70	0.94

Remark (see figure 3)

$$F_o = f_r \times \left\{ \frac{C_1}{2 \times (C_o + C_L)} + 1 \right\} \text{ (Hz)}$$



F_o : Load resonance frequency
 f_r : Resonance frequency
 R_1 : Motional resistance
 C_1 : Motional capacitance
 C_o : Shunt capacitance
 C_L : Load Capacitance

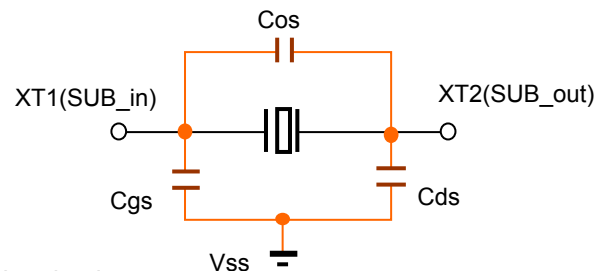
Figure 3 Equivalent circuit of crystal unit, and CL**Remark (see figure 4)**

Approximate formula of the load capacitance of the circuit CL,

$$C_L = \frac{C_g \times C_d}{C_g + C_d} + C_s \text{ (pF)}$$

$$C_s = \frac{C_{gs} \times C_{ds}}{C_{gs} + C_{ds}} + C_{os} \text{ (pF)}$$

where C_s (=1.5 to 2.5pF) stands for stray capacitance of the circuit.



C_{os} : X1_X2 Stray capacitance
 C_{gs} : X1_Vss Stray capacitance
 C_{ds} : X2_Vss Stray capacitance

Figure 4 Stray capacitance Cos,Cgs,Cds of the circuit

Resonator circuit constants differ depending on the resonator element, stray capacitance in its interconnecting circuit, and other factors. Suitable constants should be determined in consultation with the resonator element manufacturer.



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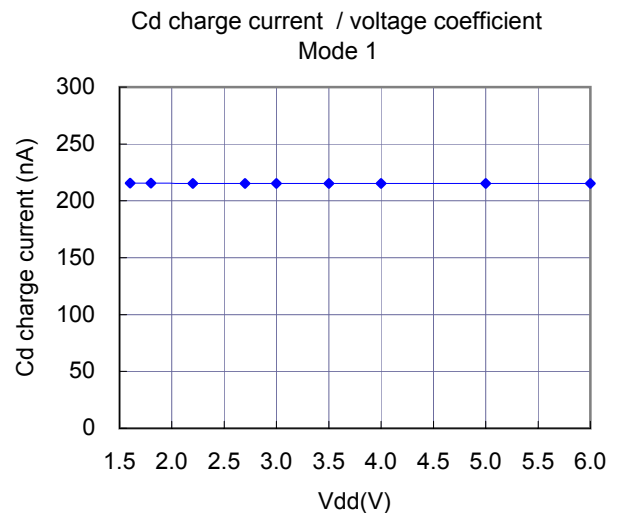
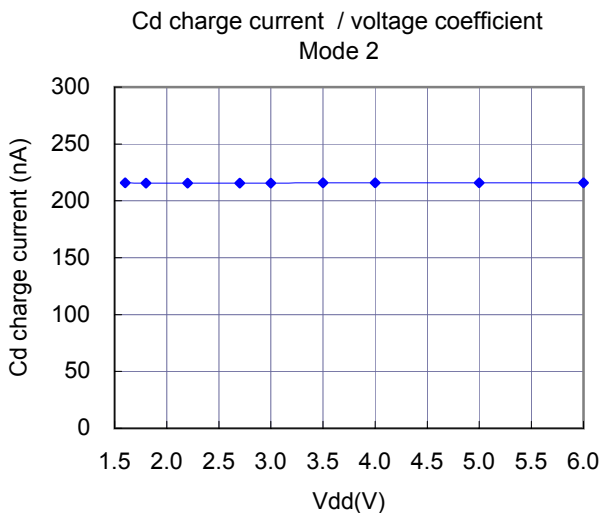
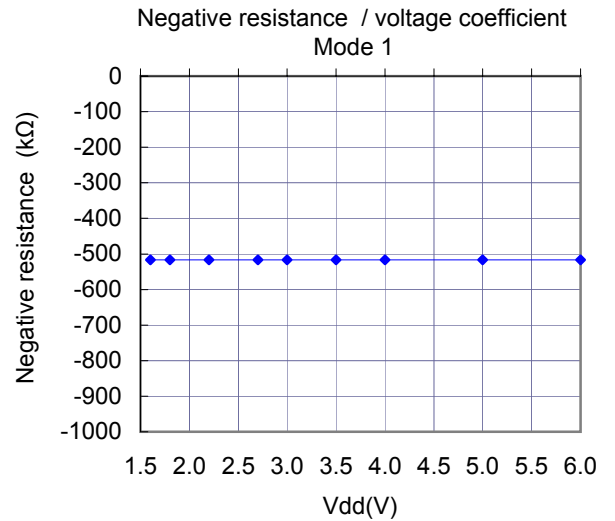
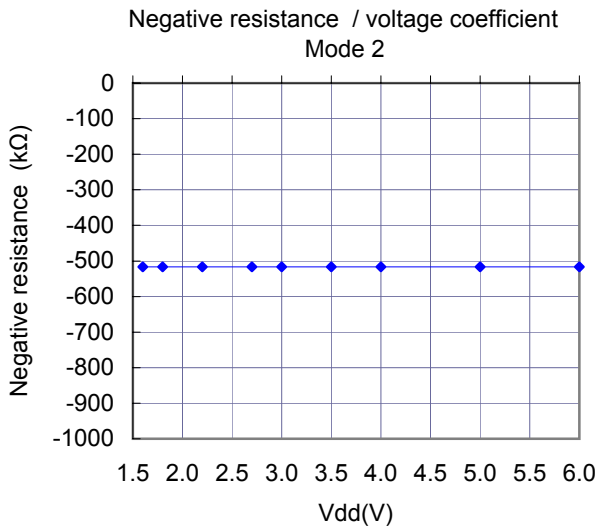
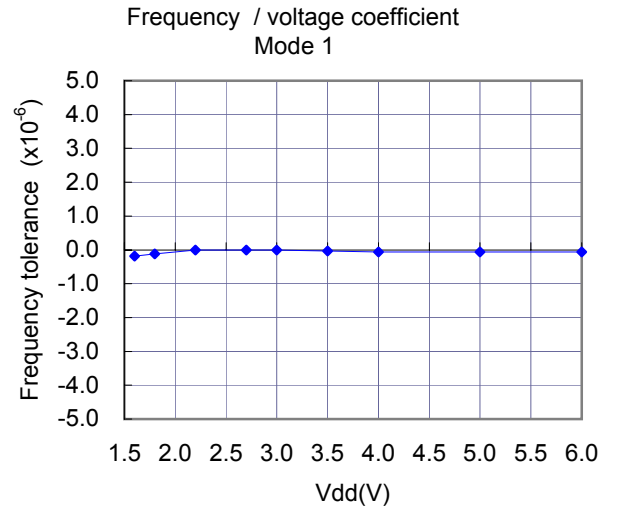
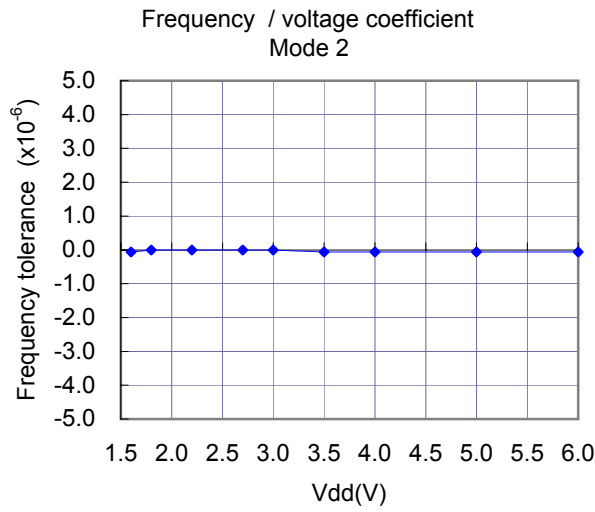
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : Vcc=(1.6V) to (6.0V) at 25°C



Low current consumption mode

Referential Data(1) : Voltage characteristics



Evaluation of a Low Frequency Clock Oscillation Circuit

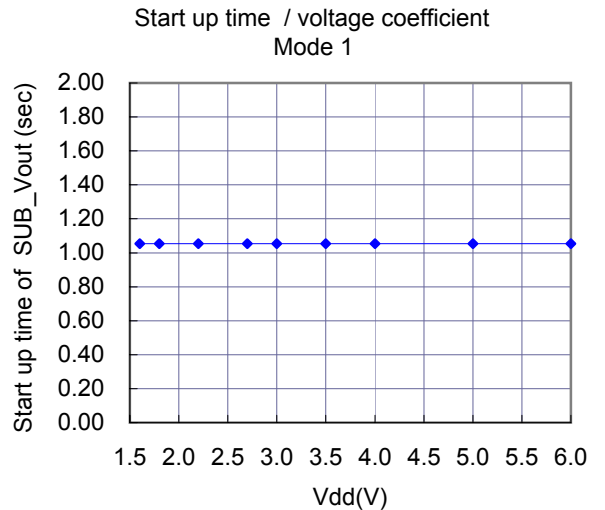
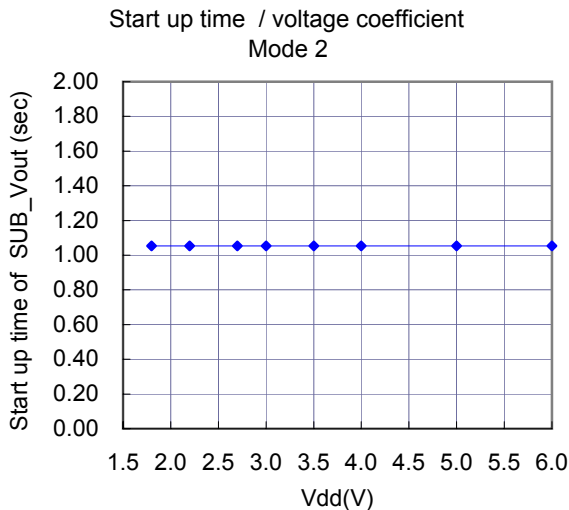
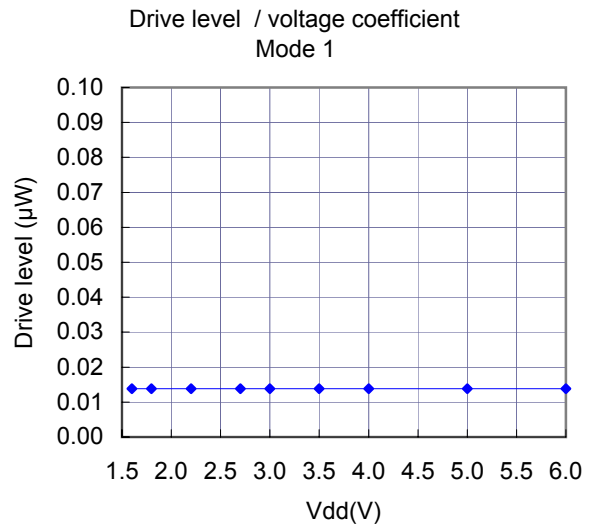
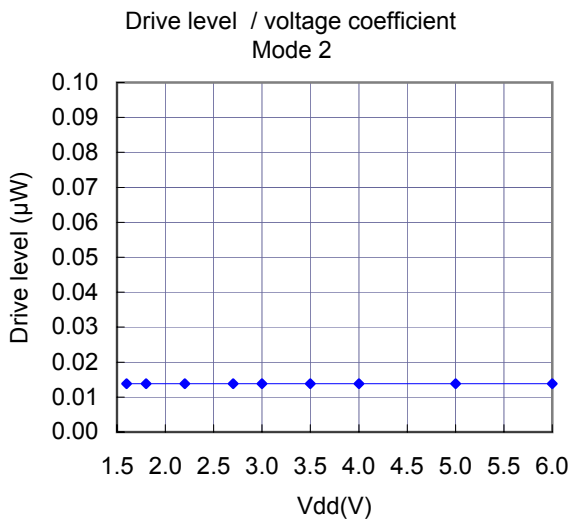
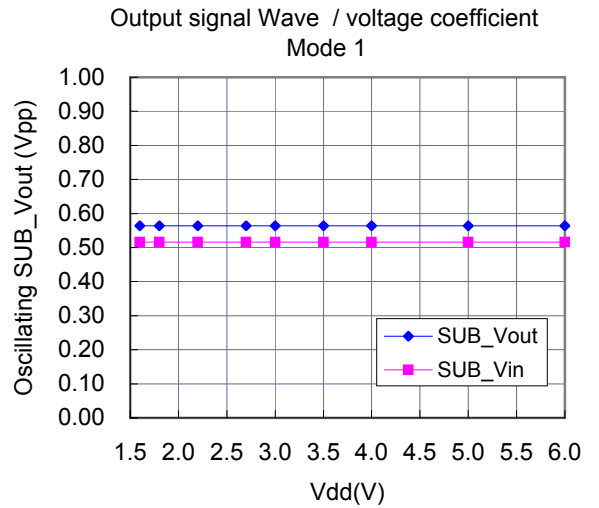
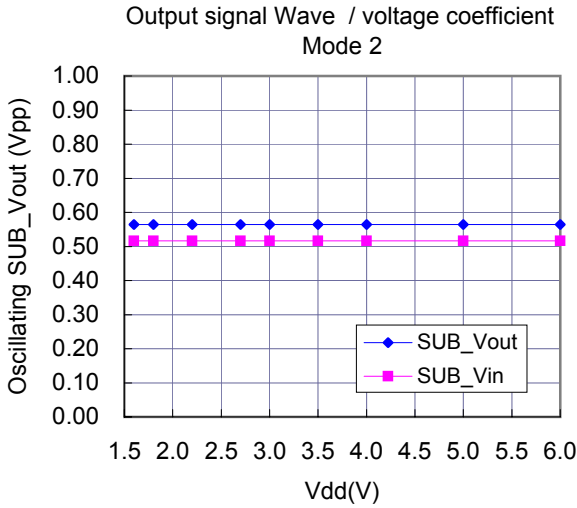
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : Vcc=(1.6V) to (6.0V) at 25°C



Low current consumption mode

Referential Data(2) : Voltage characteristics



Evaluation of a Low Frequency Clock Oscillation Circuit

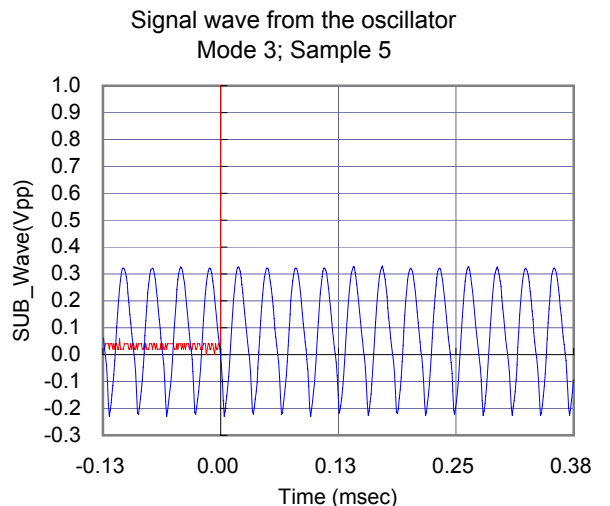
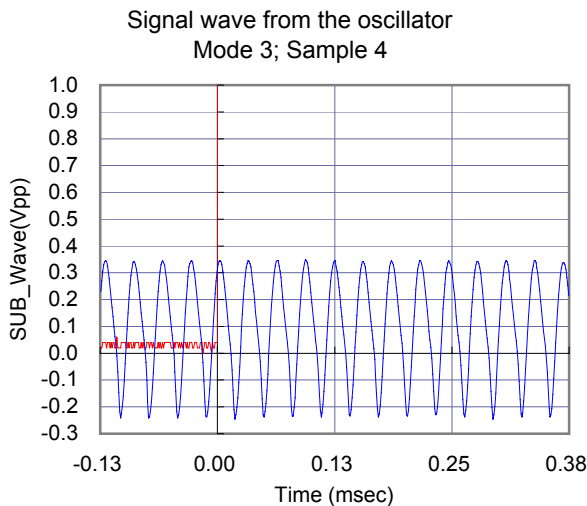
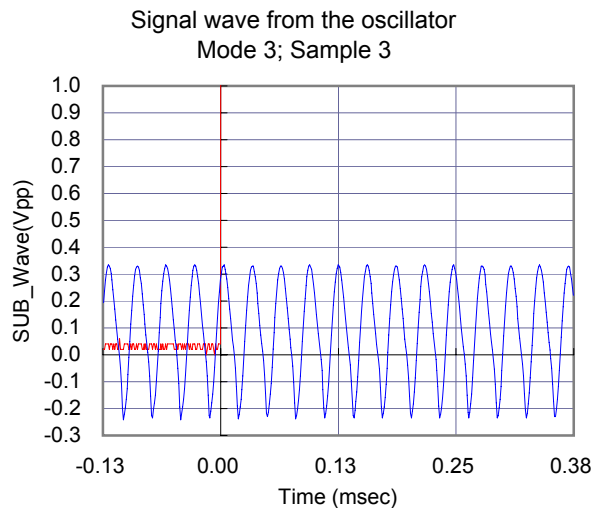
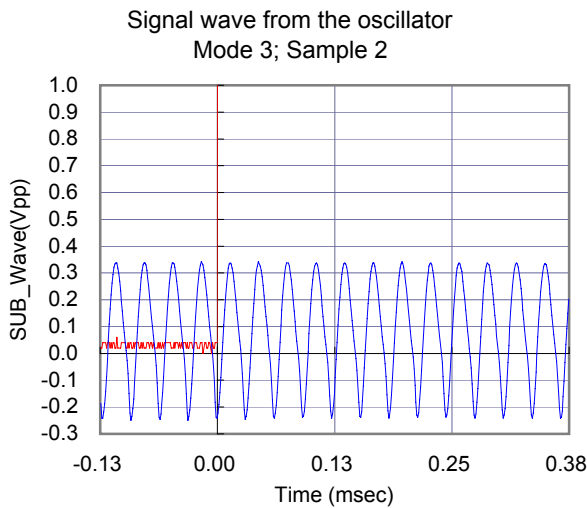
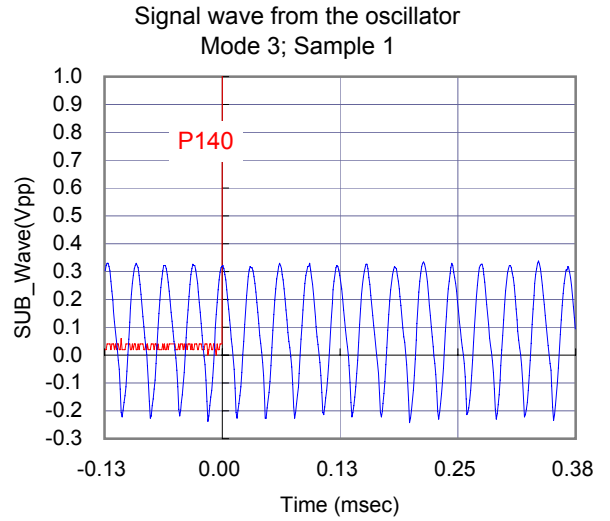
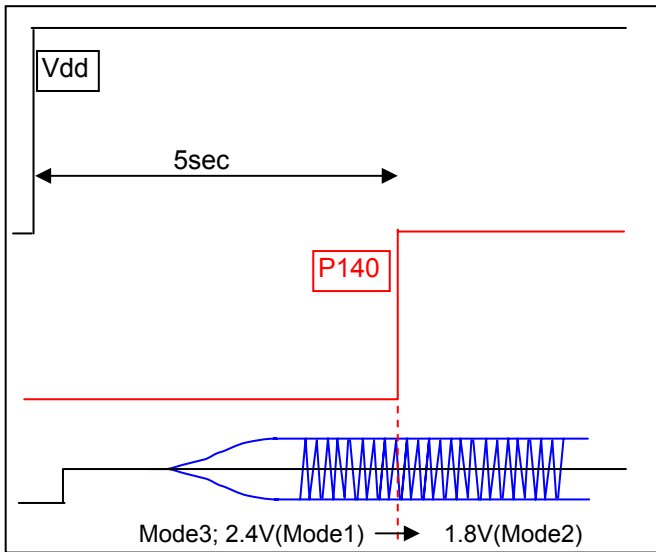
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : Vdd=3.0V at 25°C



Low current consumption mode

Referential Data(3) : Mode 3 characteristics



Evaluation of a Low Frequency Clock Oscillation Circuit

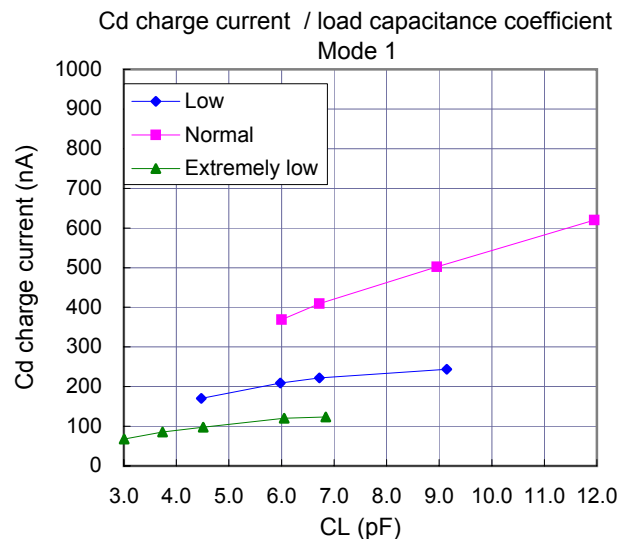
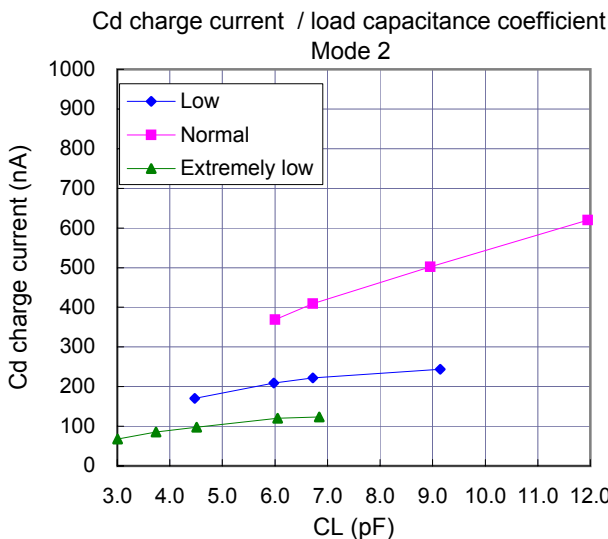
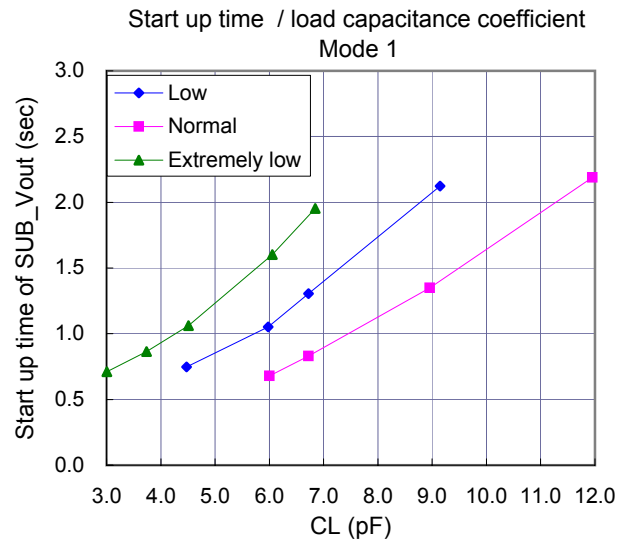
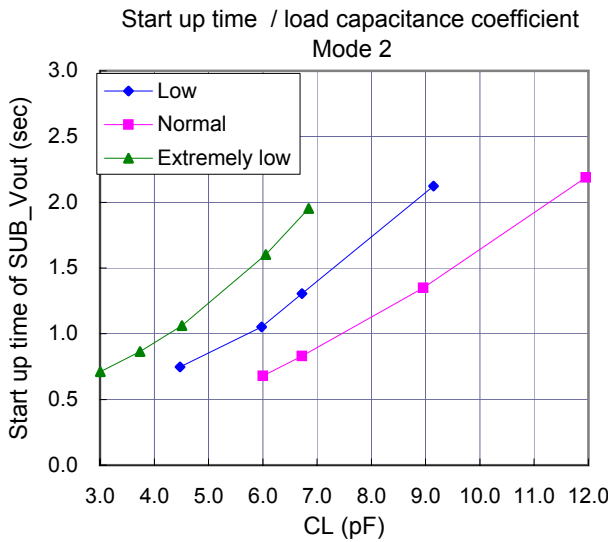
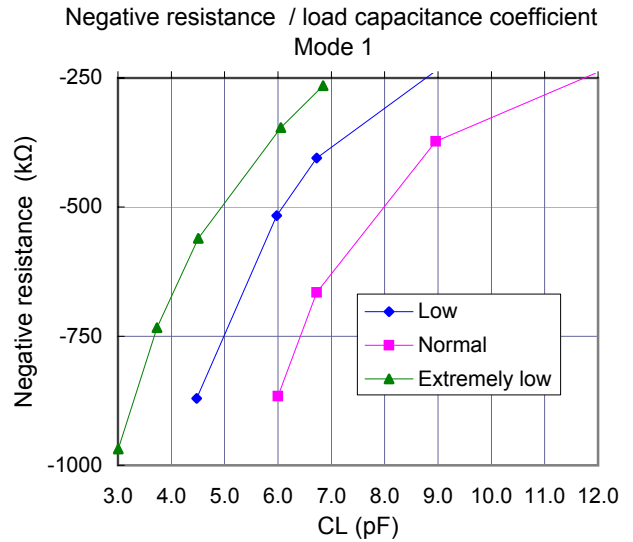
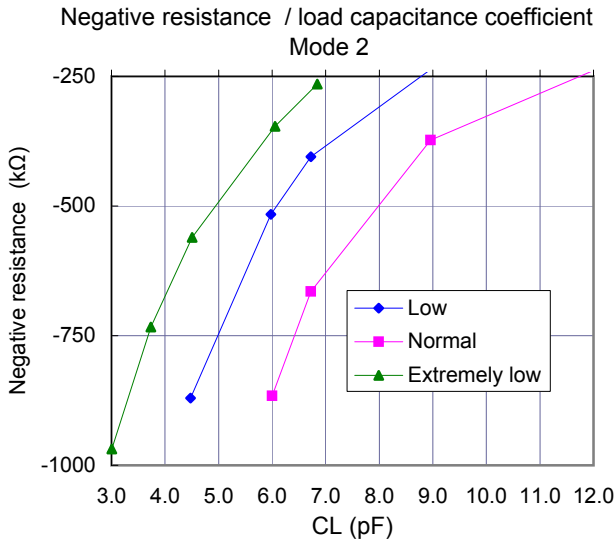
VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : V_{dd}=3.0V at 25°C



Low current consumption mode

Referential Data(4) : Load capacitance characteristics(Low,Normal,Extremely low)



Evaluation of a Low Frequency Clock Oscillation CircuitVT-200-FL 6.0pF with μ PD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]Measurement conditions : V_{dd}=3.0V at 25°C

Low current consumption mode

Referential Data(5) : IC sample characteristics(Low,Normal,Extremely low)

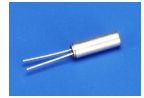


Table 1 Oscillation mode selection of XT1 oscillation circuit and load capacitance for a resonator

AMPHS1	AMPHS0	Oscillation mode selection	Recommended load capacitance (Cd charge current)
0	0	Low consumption oscillation mode.(default)	6.0pF (210nA typ)
0	1	Normal consumption oscillation mode.	6.0pF (380nA typ),12.5pF (630nA typ)
1	0	Extremely low consumption oscillation mode.	6.0pF (120nA typ),3.7pF (80nA typ)
1	1	(DC bias current; 200nA max. at 25°C)	3.0pF (65pF typ)

*78K0R/Kx3-L series; μ PD78F1010 to μ PD78F1014 & VT-200 seriesLow current consumption mode; IC sample Rd=0 Ω ,Cg=9pF,Cd=8pF,CL=6.0pF

MODE	IC sample	Fosc(Hz)	df / f(x10-6)	DL(μ W)	M(times)	XT2(Vpp)	I _d (nA)	Vstart(V)	T _s (sec)
Mode 1 (2.4V)	Sample 1	32768.07	1.4	0.015	9	0.58	220	1.58	1.19
	Sample 2	32768.06	1.1	0.014	10	0.54	204	1.58	1.17
	Sample 3	32768.06	1.1	0.014	10	0.56	213	1.61	1.05
	Sample 4	32768.06	1.2	0.010	11	0.59	223	1.59	0.93
	Sample 5	32768.05	0.9	0.011	10	0.54	205	1.59	0.94
Mode 2 (1.8V)	Sample 1	32768.06	1.2	0.015	9	0.58	220	1.67	1.19
	Sample 2	32768.05	0.8	0.014	10	0.54	204	1.68	1.17
	Sample 3	32768.05	0.9	0.014	10	0.56	213	1.72	1.05
	Sample 4	32768.05	1.0	0.010	11	0.59	223	1.69	0.93
	Sample 5	32768.04	0.7	0.011	10	0.54	206	1.70	0.94

Normal current consumption mode; IC sample Rd=0 Ω ,Cg=9pF,Cd=8pF,CL=6.0pF

MODE	IC sample	Fosc(Hz)	df / f(x10-6)	DL(μ W)	M(times)	XT2(Vpp)	I _d (nA)	Vstart(V)	T _s (sec)
Mode 1 (2.4V)	Sample 1	32768.04	0.7	0.019	15	1.00	412	1.58	0.77
	Sample 2	32768.04	0.7	0.019	16	0.96	394	1.58	0.77
	Sample 3	32768.03	0.2	0.016	17	0.92	381	1.61	0.68
	Sample 4	32768.02	0.0	0.013	19	0.84	350	1.59	0.71
	Sample 5	32768.01	-0.4	0.012	16	0.83	344	1.59	0.70
Mode 2 (1.8V)	Sample 1	32768.04	0.5	0.019	15	1.00	412	1.67	0.77
	Sample 2	32768.04	0.6	0.019	16	0.96	395	1.68	0.77
	Sample 3	32768.02	0.1	0.016	17	0.92	381	1.72	0.68
	Sample 4	32768.02	-0.2	0.013	19	0.84	351	1.69	0.71
	Sample 5	32768.00	-0.5	0.013	16	0.83	345	1.70	0.70

Extremely low current consumption mode; IC sample Rd=0 Ω ,Cg=9pF,Cd=8pF,CL=6.0pF

MODE	IC sample	Fosc(Hz)	df / f(x10-6)	DL(μ W)	M(times)	XT2(Vpp)	I _d (nA)	Vstart(V)	T _s (sec)
Mode 1 (2.4V)	Sample 1	32768.02	0.1	0.007	6	0.35	122	1.58	1.79
	Sample 2	32767.99	-0.8	0.006	6	0.32	112	1.58	1.69
	Sample 3	32768.03	0.2	0.006	7	0.34	119	1.61	1.60
	Sample 4	32768.03	0.2	0.007	8	0.37	127	1.59	1.39
	Sample 5	32768.01	-0.3	0.006	6	0.33	114	1.59	1.39
Mode 2 (1.8V)	Sample 1	32768.02	-0.2	0.007	6	0.35	123	1.67	1.79
	Sample 2	32767.99	-1.1	0.006	6	0.32	113	1.68	1.69
	Sample 3	32768.02	0.0	0.006	7	0.34	119	1.72	1.60
	Sample 4	32768.02	0.1	0.007	8	0.37	128	1.69	1.39
	Sample 5	32768.00	-0.5	0.006	6	0.33	115	1.70	1.39

Evaluation of a Low Frequency Clock Oscillation Circuit

VT-200-FL 6.0pF with uPD78F1014GC-16BT [LQFP(14x14) 0.5mm pitch]

Measurement conditions : Vdd=3.0V at 25°C

Referential Data(5) : Selection of XT1 oscillation mode and recommended load capacitance

For 78K0R/Kx3-L series

XT1 oscillation circuit has the function (via software) to select the XT1 oscillation mode.

The XT1 oscillation mode can be switched over just one time among normal oscillation (Normal), low consumption oscillation (Low), and "extremely low consumption oscillation" mode (E-Low).

Correlations between the oscillation mode selection of XT1 oscillation circuit and the recommended load capacitance for a resonator are shown in Table 1 for safety use.

VT-200 series

VT-200-FL CL=3.0pF,3.7pF,4.4pF,6.0pF and VT-200-F CL=8.7pF,12.5pF

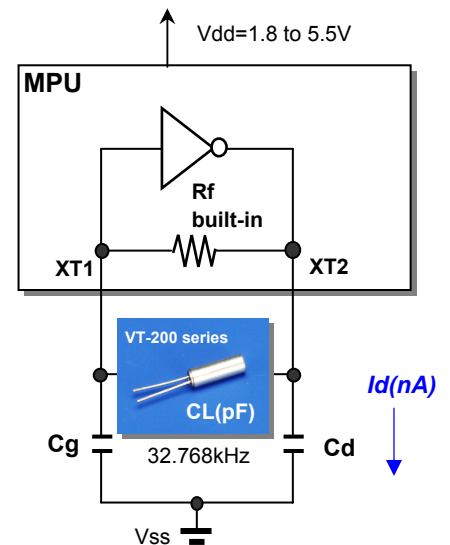


Table 1 Oscillation mode selection of XT1 oscillation circuit and load capacitance for a resonator

XT1 oscillation mode selection	Recommended circuit constant and load capacitance for a resonator				
	Cg=3pF,Cd=2pF	Cg=4pF,Cd=3pF	Cg=9pF,Cd=8pF	Cg=15pF,Cd=13pF	Cg=20pF,Cd=20pF
E-Low Extremely low consumption oscillation	VT-200-FL 3.0pF <i>Id=65nA typ</i> <i>RL=-990kΩ typ</i> <i>Ts=0.75sec typ</i>	VT-200-FL 3.7pF <i>Id=80nA typ</i> <i>RL=-730kΩ typ</i> <i>Ts=0.85sec typ</i>	VT-200-FL 6.0pF <i>Id=120nA typ</i> <i>RL=-335kΩ typ</i> <i>Ts=1.55sec typ</i>	<i>Not recommended.</i>	<i>Not recommended.</i>
Low Low consumption oscillation	<i>Not recommended.</i>	<i>Not recommended.</i>	VT-200-FL 6.0pF <i>Id=210nA typ</i> <i>RL=-490kΩ typ</i> <i>Ts=1.05sec typ</i>	<i>Not recommended.</i>	<i>Not recommended.</i>
Normal Normal oscillation	<i>Not recommended.</i>	<i>Not recommended.</i>	VT-200-FL 6.0pF <i>Id=380nA typ</i> <i>RL=-830kΩ typ</i> <i>Ts=0.75sec typ</i>	VT-200-F 8.7pF <i>Id=500nA typ</i> <i>RL=-420kΩ typ</i> <i>Ts=1.20sec typ</i>	VT-200-F 12.5pF <i>Id=630nA typ</i> <i>RL=-250kΩ typ</i> <i>Ts=2.15sec typ</i>

NEC MPU 78K0R/Kx3-L series

- 78K0R/KF3-L; uPD78F1010
- uPD78F1011
- uPD78F1012
- 78K0R/KG3-L; uPD78F1013
- uPD78F1014

